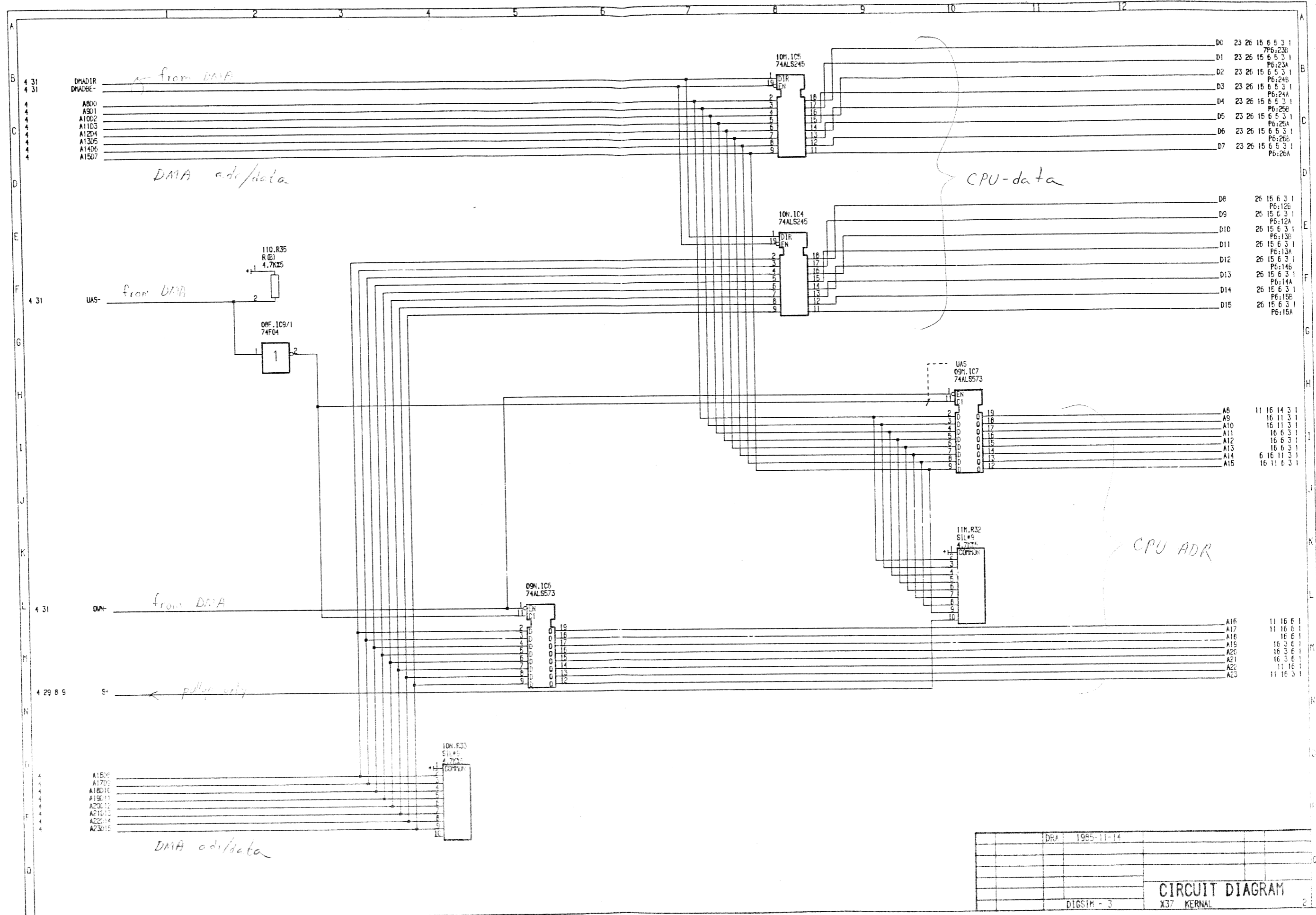


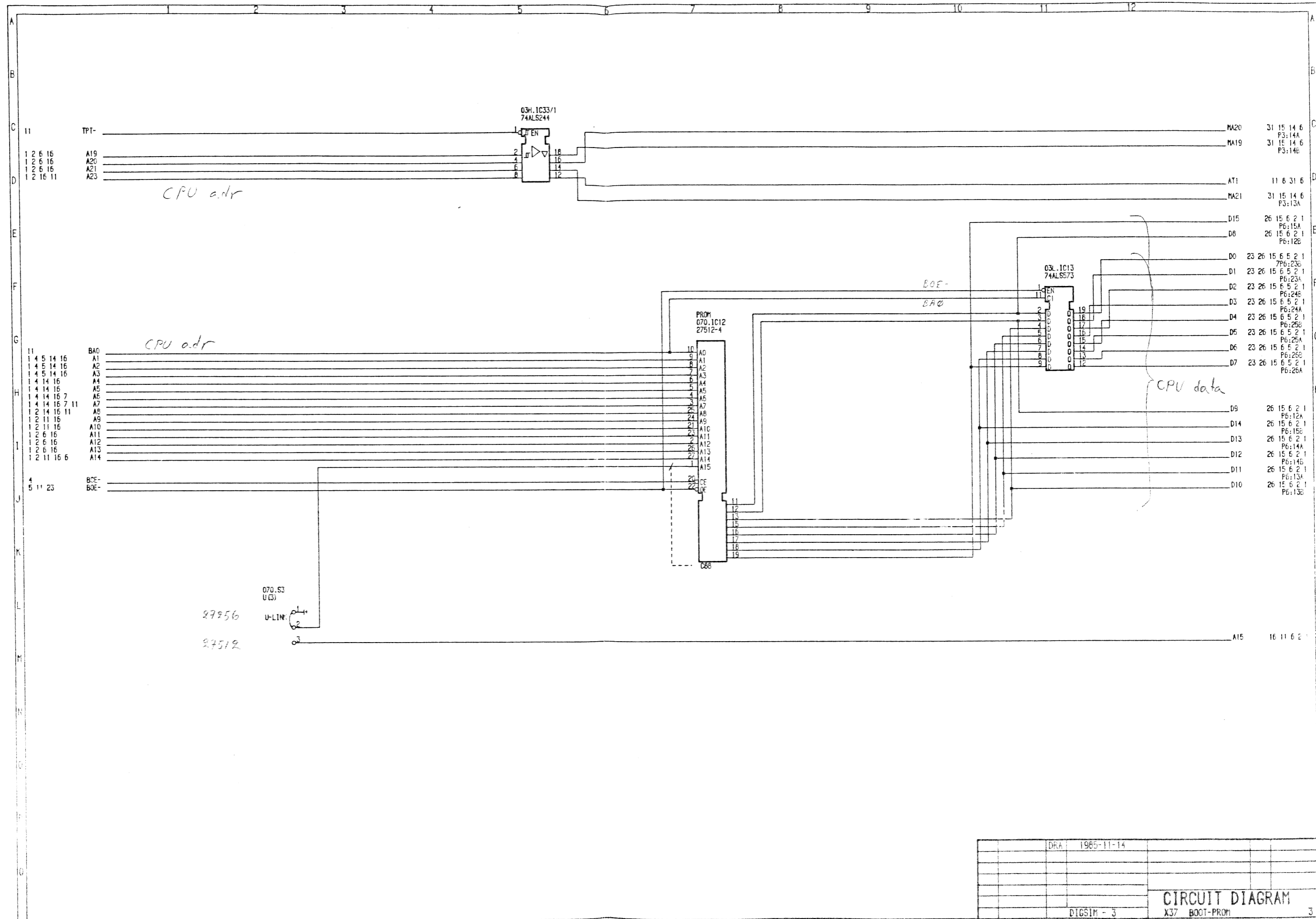
CPU data

DRAM data,
ECC - 90, check

DRA	1985-11-14	CPU + FPP
CIRCUIT DIAGRAM		
X37 KERNEL		
DIOSIM - 3		



DRA	1985-11-14		
DIGSIM - 3		CIRCUIT DIAGRAM	
X37		KERNAL	



11
1 2 6 16
1 2 6 16
1 2 6 16
1 2 16 11

11
1 4 5 14 16
1 4 5 14 16
1 4 5 14 16
1 4 14 16
1 4 14 16
1 4 14 16 7
1 4 14 16 7 11
1 2 11 16
1 2 11 16
1 2 6 16
1 2 6 16
1 2 6 16
1 2 11 16 6

4
5 11 23

TPT-

A19
A20
A21
A23

BA0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14

BCE-
BDE-

CPU adr

CPU adr

070.53
U(3)

27512

U-LIN

03H. IC33/1
74ALS244

PROM
070. IC12
27512-4

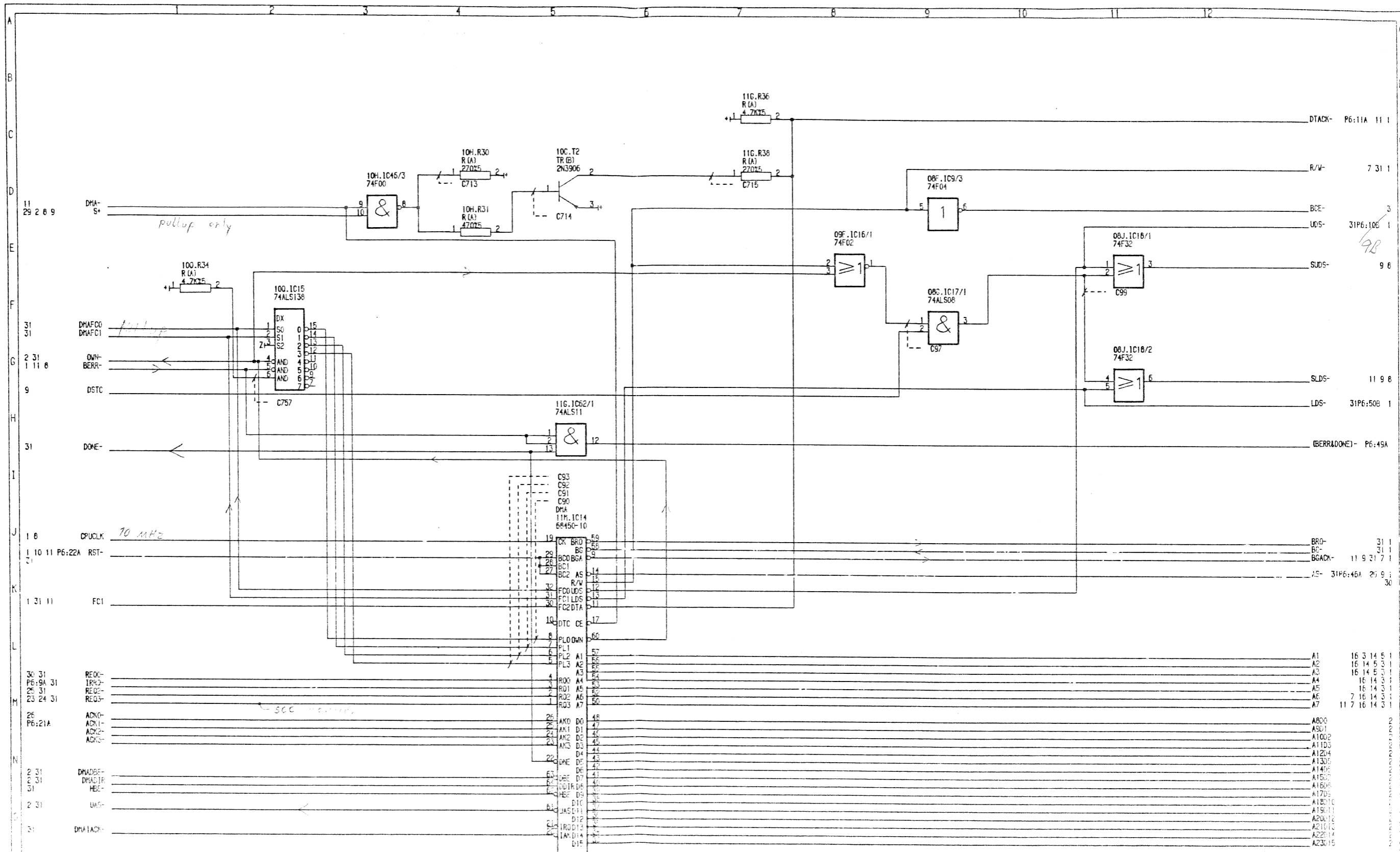
03H. IC13
74ALS573

MA20 31 15 14 6
MA19 31 15 14 6
AT1 11 6 31 6
MA21 31 15 14 6
P3:13A
D15 26 15 6 2 1
D8 26 15 6 2 1
P6:12B
D0 23 26 15 6 5 2 1
D1 23 26 15 6 5 2 1
P6:23C
D2 23 26 15 6 5 2 1
P6:24E
D3 23 26 15 6 5 2 1
P6:24A
D4 23 26 15 6 5 2 1
P6:25B
D5 23 26 15 6 5 2 1
P6:25A
D6 23 26 15 6 5 2 1
P6:26E
D7 23 26 15 6 5 2 1
P6:26A
D9 26 15 6 2 1
P6:12A
D14 26 15 6 2 1
P6:15E
D13 26 15 6 2 1
P6:14A
D12 26 15 6 2 1
P6:14E
D11 26 15 6 2 1
P6:13A
D10 26 15 6 2 1
P6:13E

A15 16 11 6 2

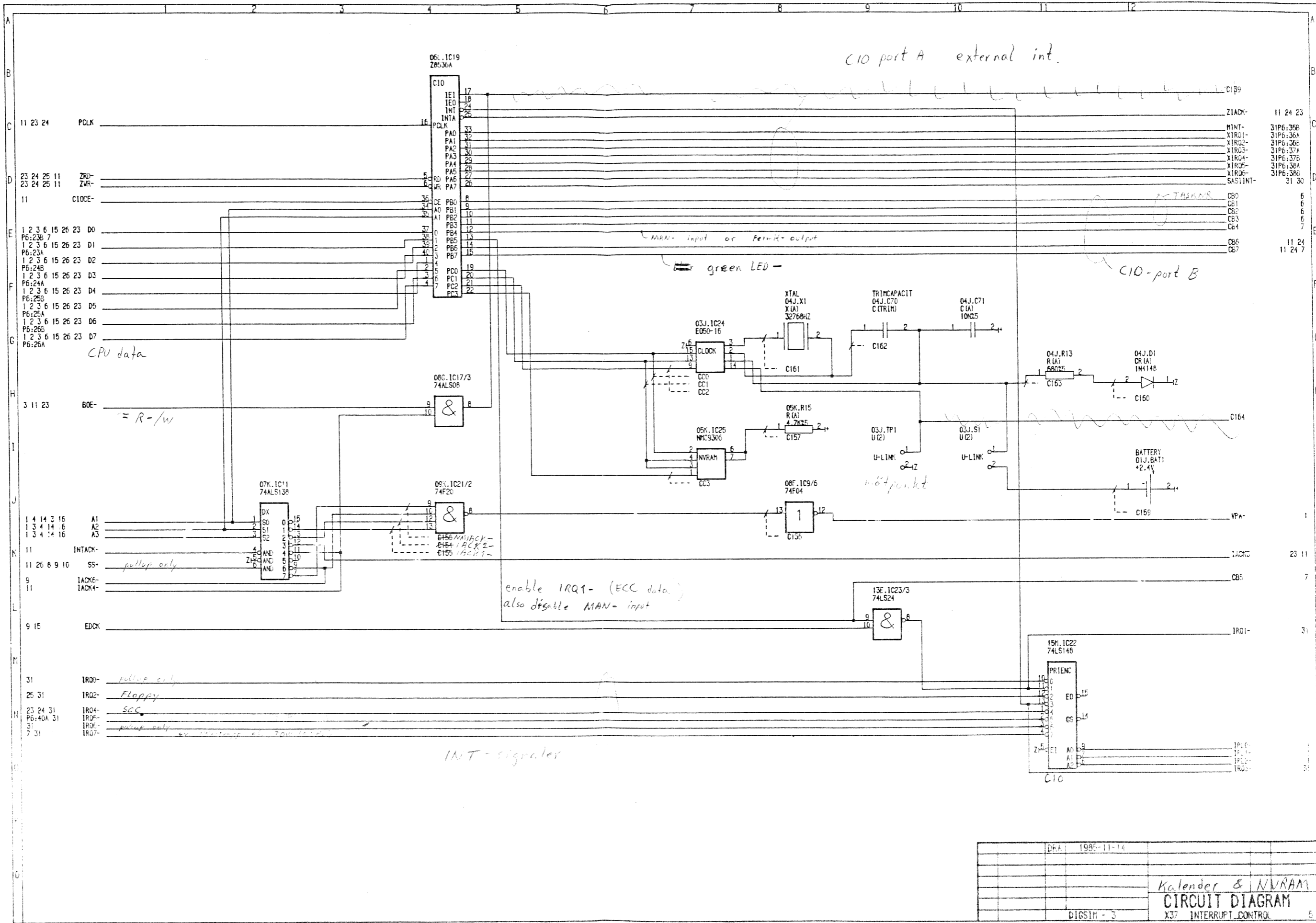
CPU data

DRA	1985-11-14
CIRCUIT DIAGRAM	
DIGSIM - 3	X37 BOOT-PROM



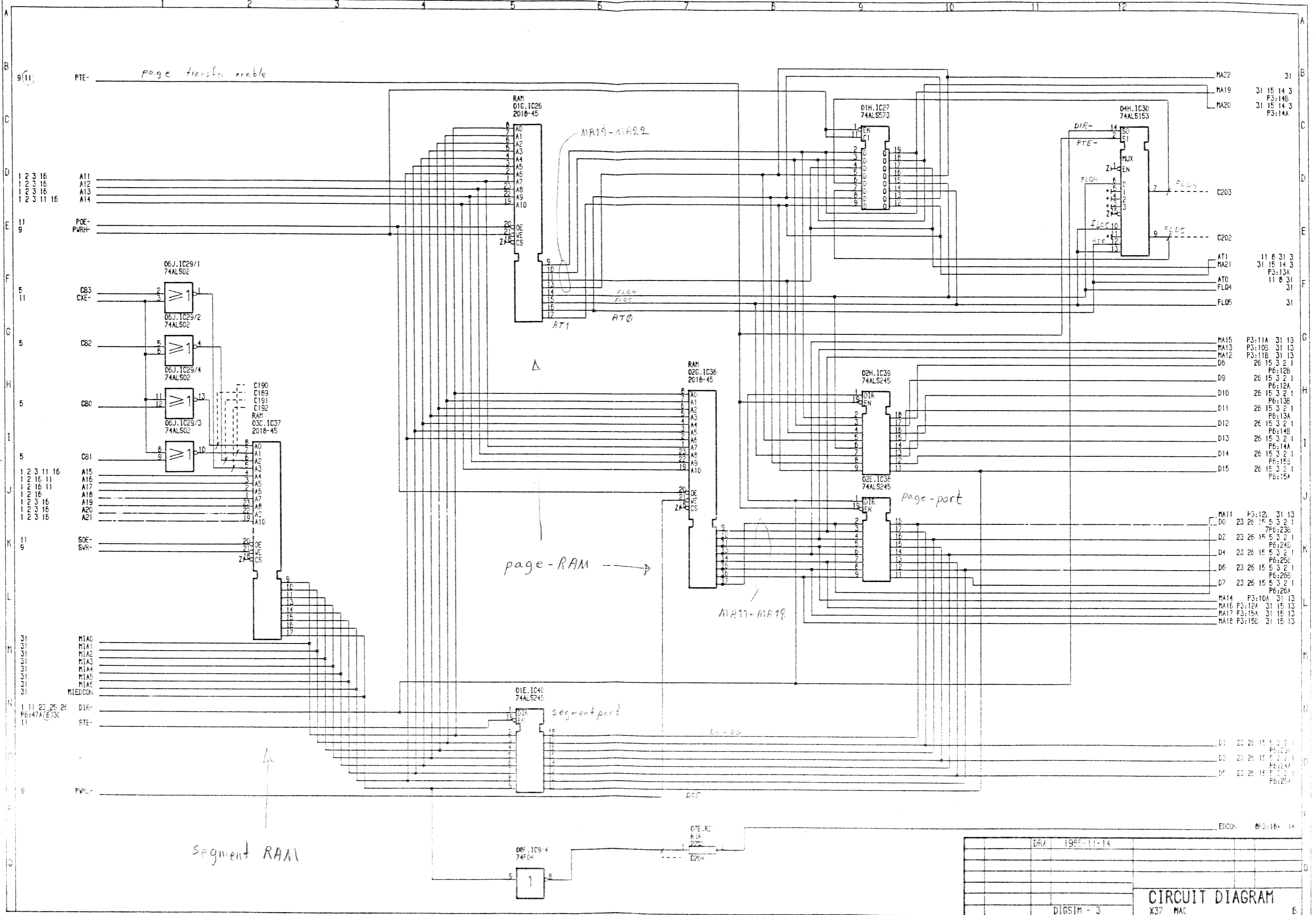
DMA - adr/data

DRA	1985-11-14		
		CIRCUIT DIAGRAM	
		X37 DMA	



DRA	1985-11-14
Kalender & NVRAM CIRCUIT DIAGRAM	
DIGSIH - 3	X37 INTERRUPT_CONTROL

AT1	AT0	R/O MEM
0	0	R/W MEM
0	1	NONE
1	0	R/W 1/0
1	1	

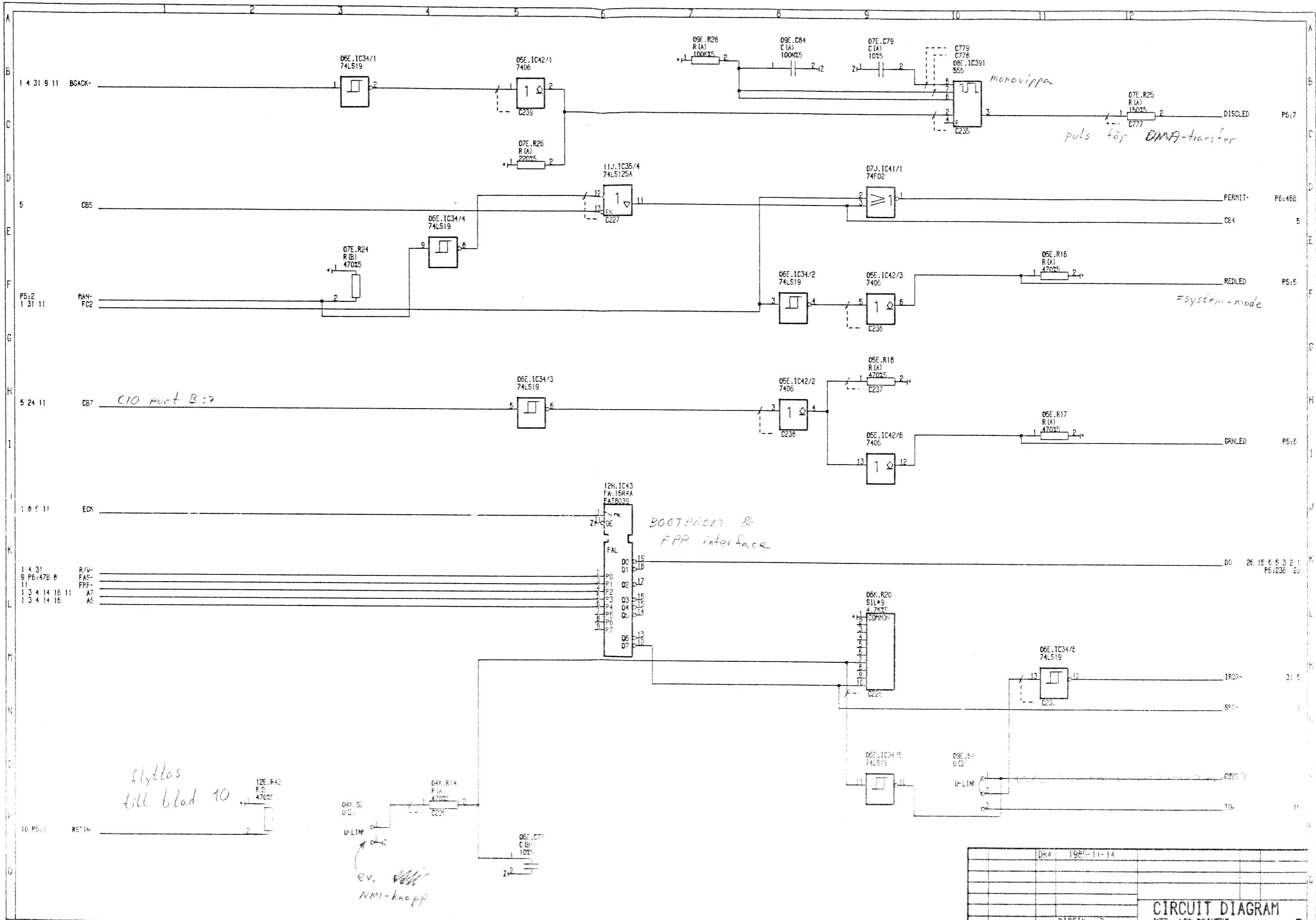


Task nr från CIO port B

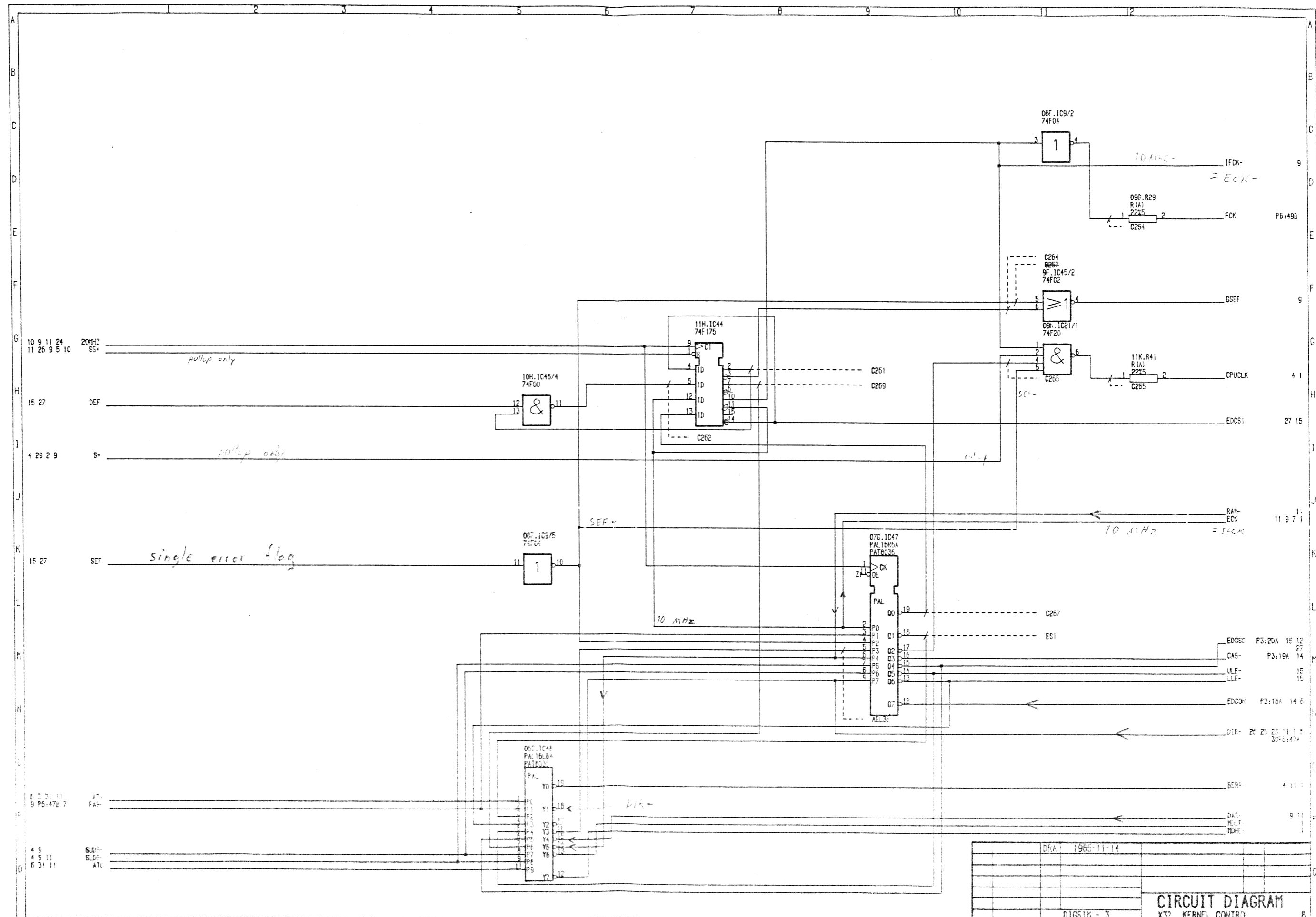
segment från CIO

Till pullop enbart

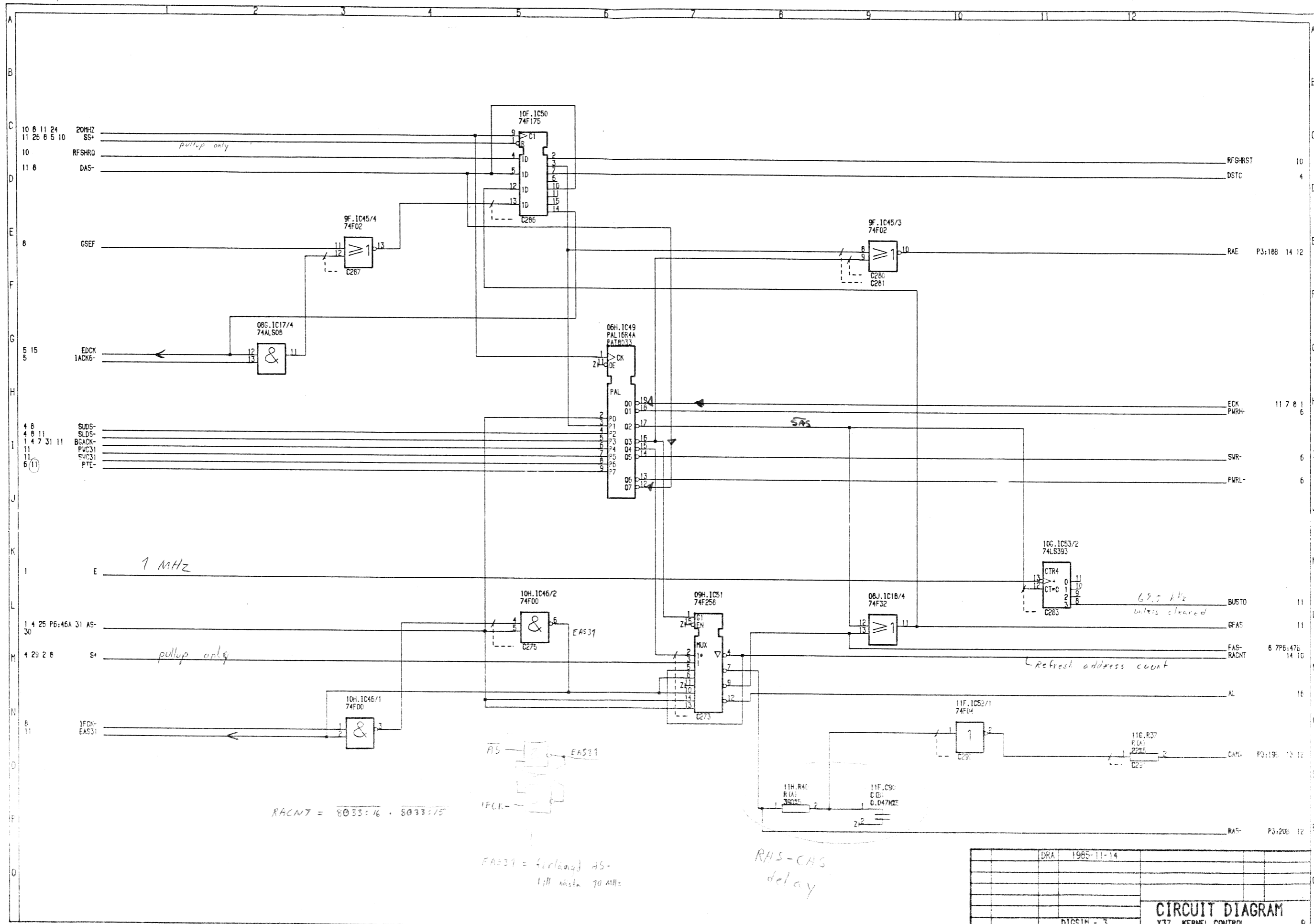
DRP	1985-11-14
CIRCUIT DIAGRAM	
DIGSIM - 3 X37 MAC	



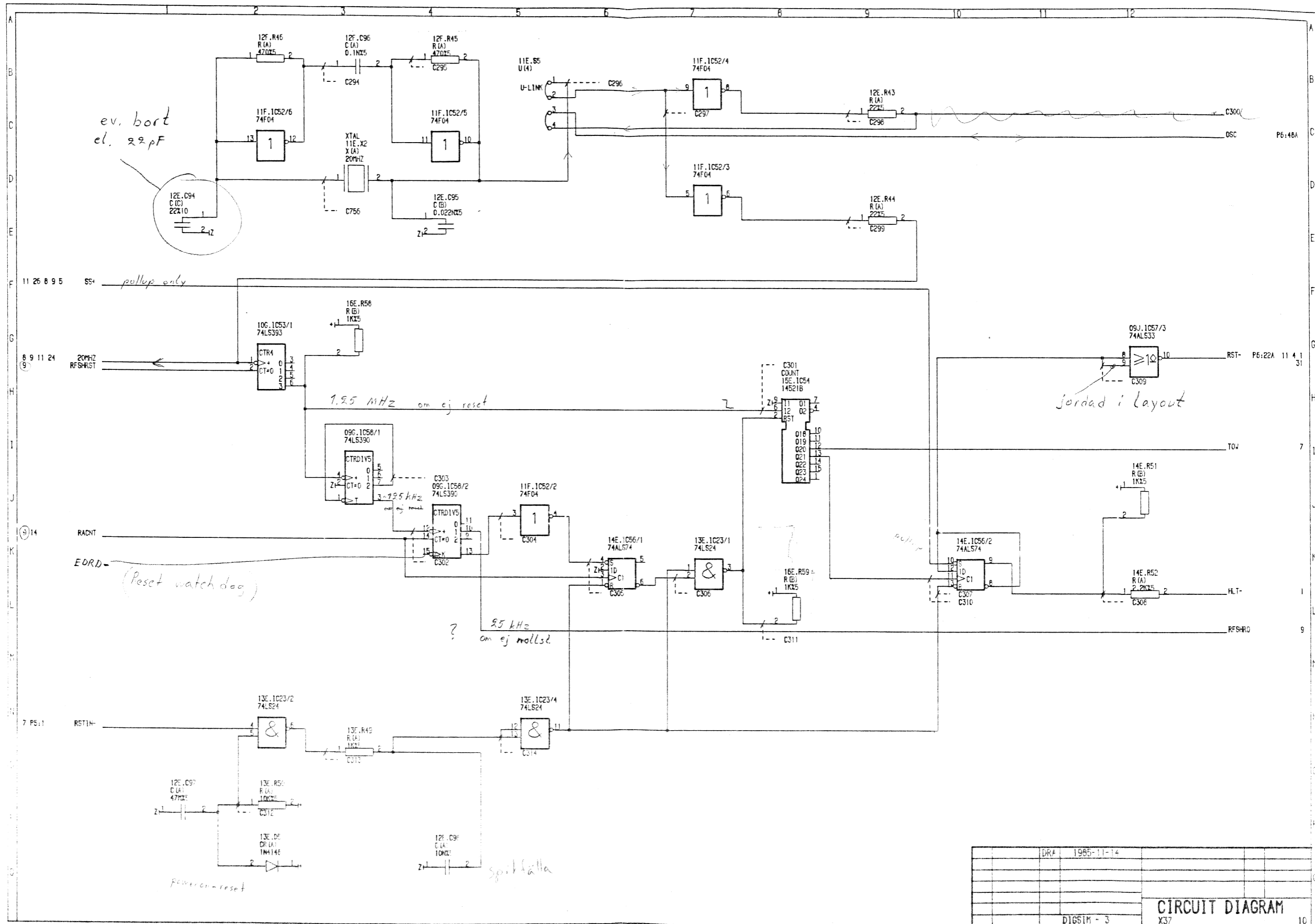
DRA	1985-11-14
CIRCUIT DIAGRAM	
X37 LED DRIVERS	
PAGE 7	



DRA	1985-11-14		
DIGSIM - 3		CIRCUIT DIAGRAM	
		X37 KERNEL CONTROL	



DRA	1985-11-14
CIRCUIT DIAGRAM	
X37 KERNEL CONTROL	
DIGSIM - 3	



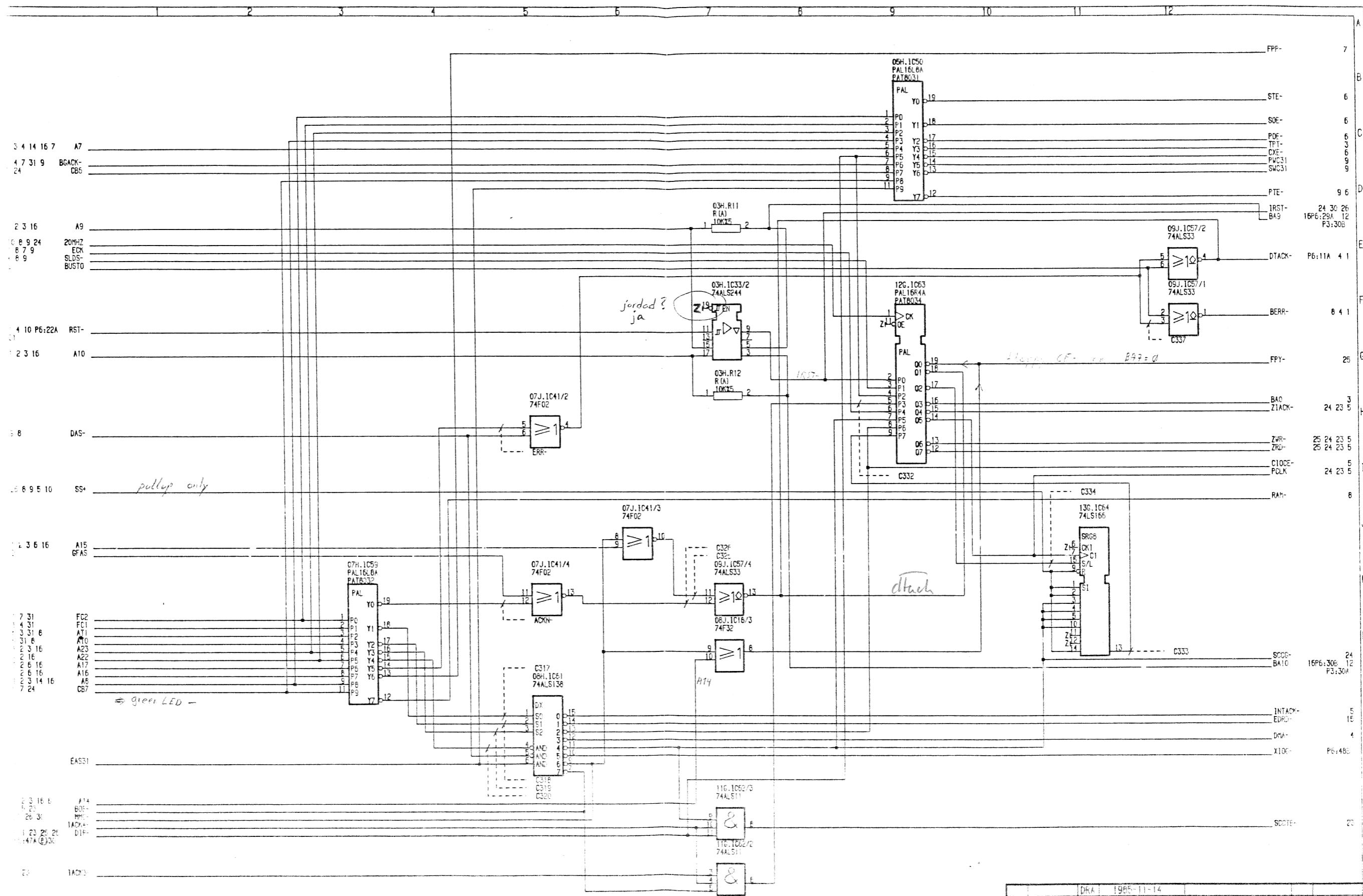
DR#	1985-11-14		
CIRCUIT DIAGRAM			
DIGSIM - 3		X37	

P6:46A

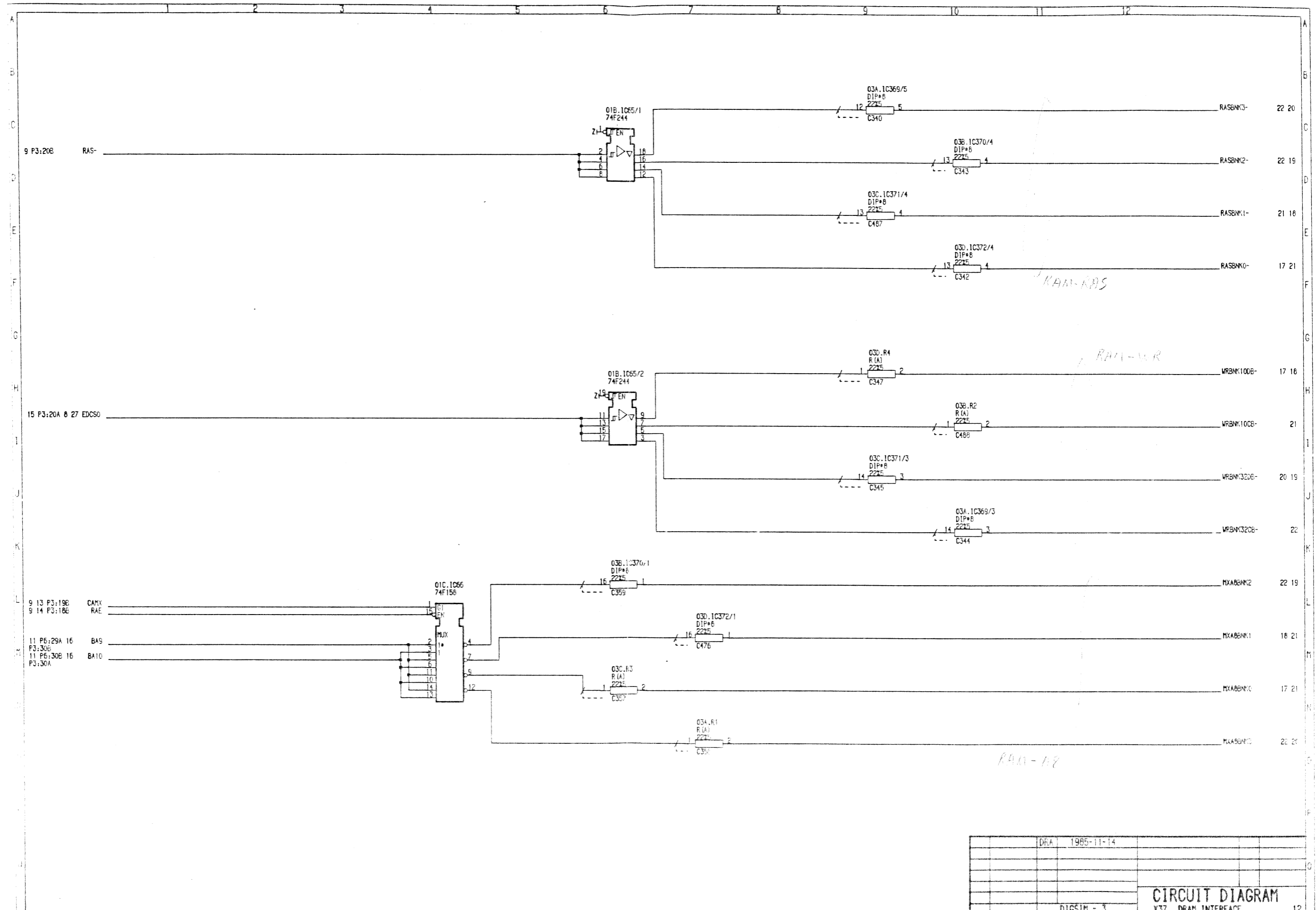
P6:22A 11 4 1 31

9

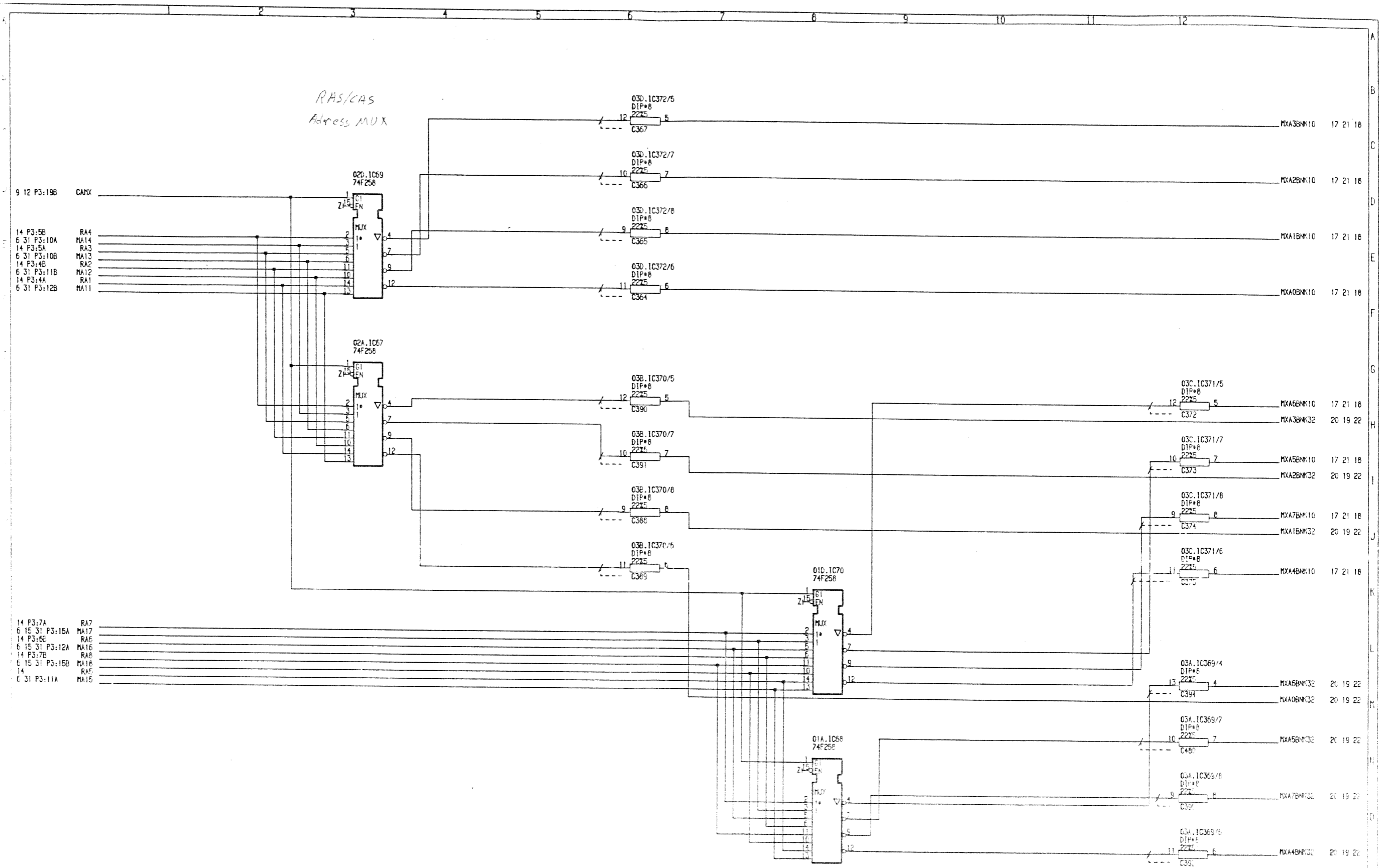
10



DRA	1985-11-14
CIRCUIT DIAGRAM	
X37 KERNEL CONTROL	
DIGSIM - 3	11

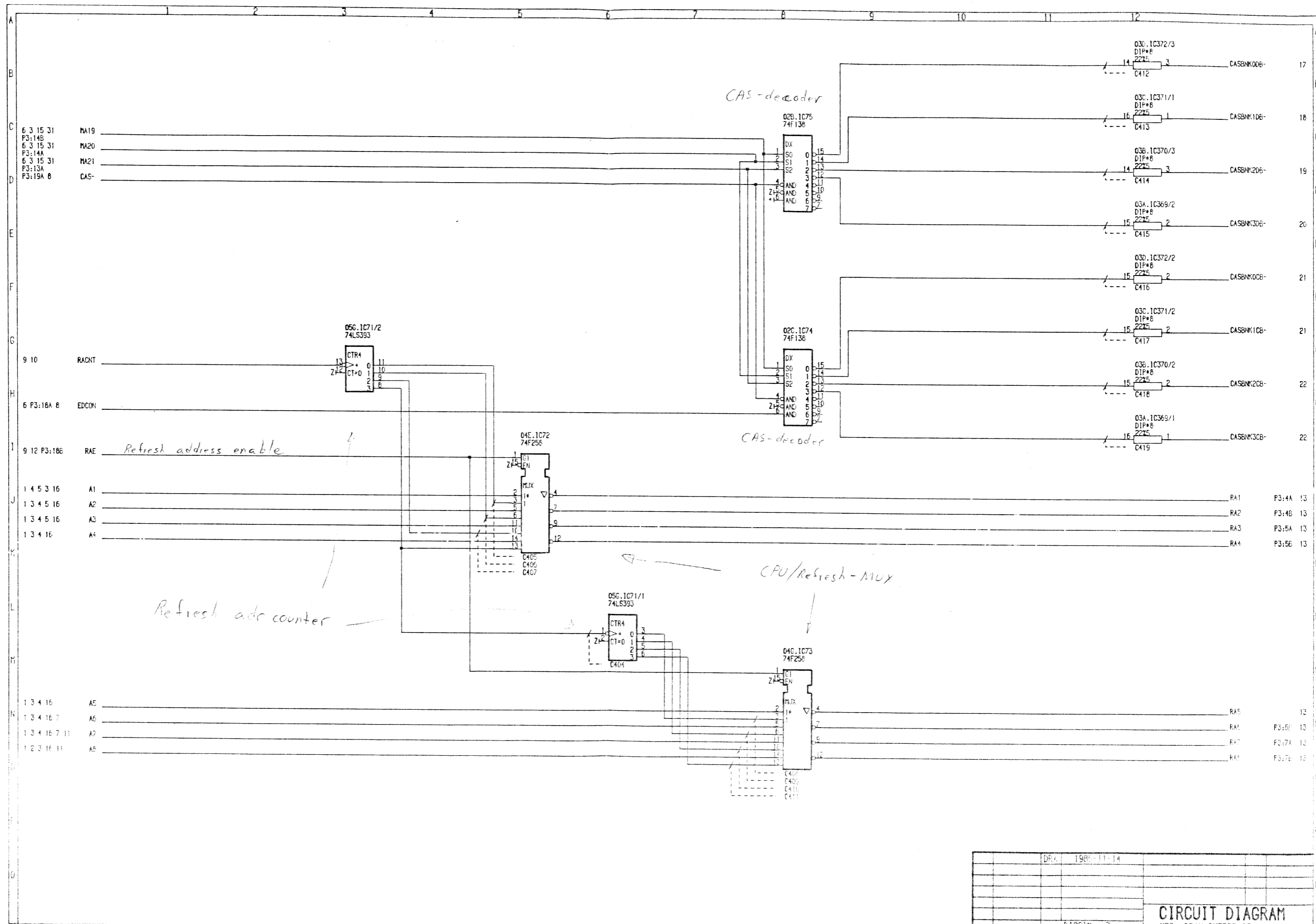


DRA	1985-11-14			
DIGSIM - 3			CIRCUIT DIAGRAM	
			X37 DRAM_INTERFACE	
			12	



*RAS/CAS
address MUX*

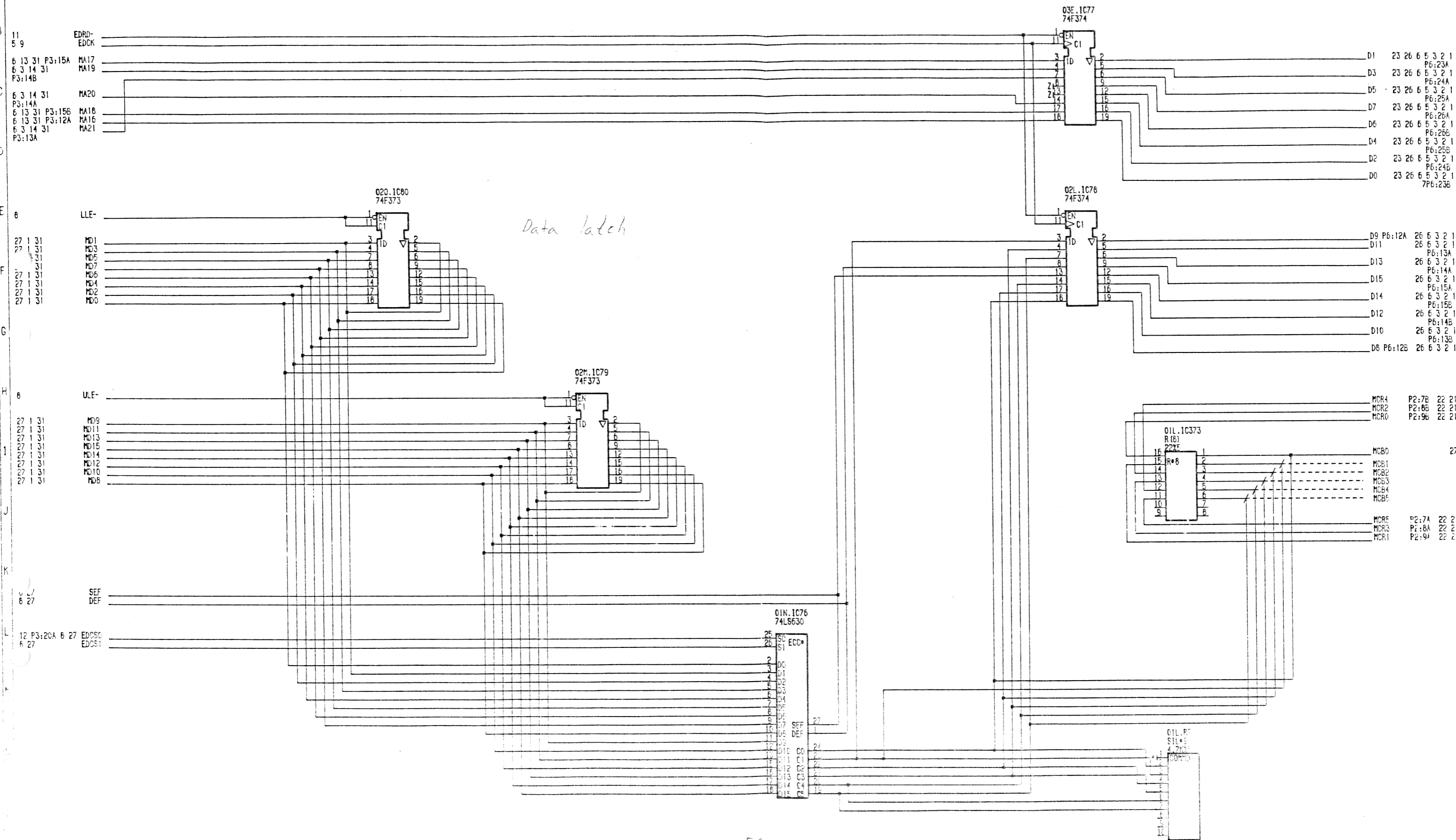
DRAWN	1965-11-14		
DESIGNED			
CHECKED			
APPROVED			
DIGSIM - 3		CIRCUIT DIAGRAM	
		X37 DRAM_INTERFACE	
		13	



DR. 1980-11-14			

CIRCUIT DIAGRAM
 X37 DRAM_INTERFACE

ECC error detect latch

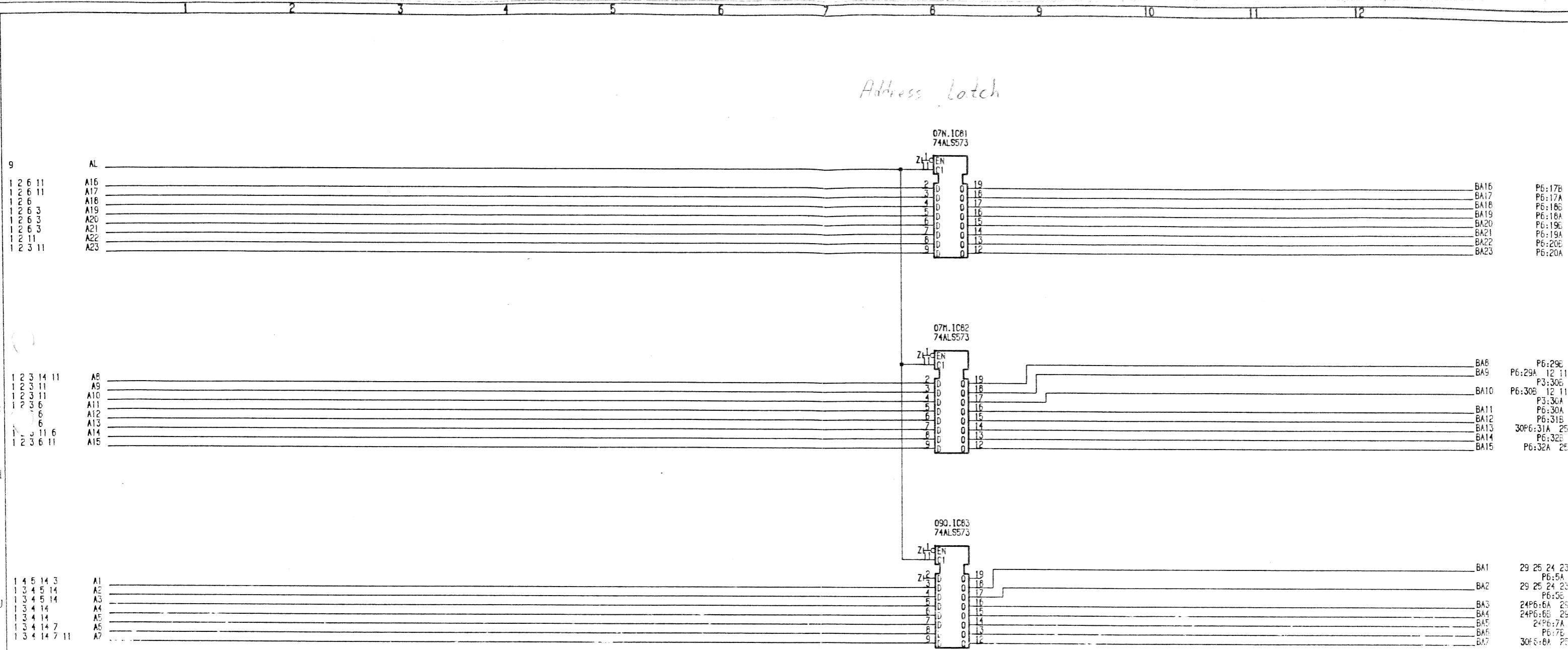


Data latch

ECC check/fixer

DRA	1985-11-14
CIRCUIT DIAGRAM	
X37 DRAM INTERFACE	
DIGSIM - 3	

Address Latch



9
1 2 6 11
1 2 6 11
1 2 6 3
1 2 6 3
1 2 11
1 2 3 11

F
G
1 2 3 14 11
1 2 3 11
1 2 3 11
1 2 3 6
1 2 3 6
1 2 3 11 6
1 2 3 6 11

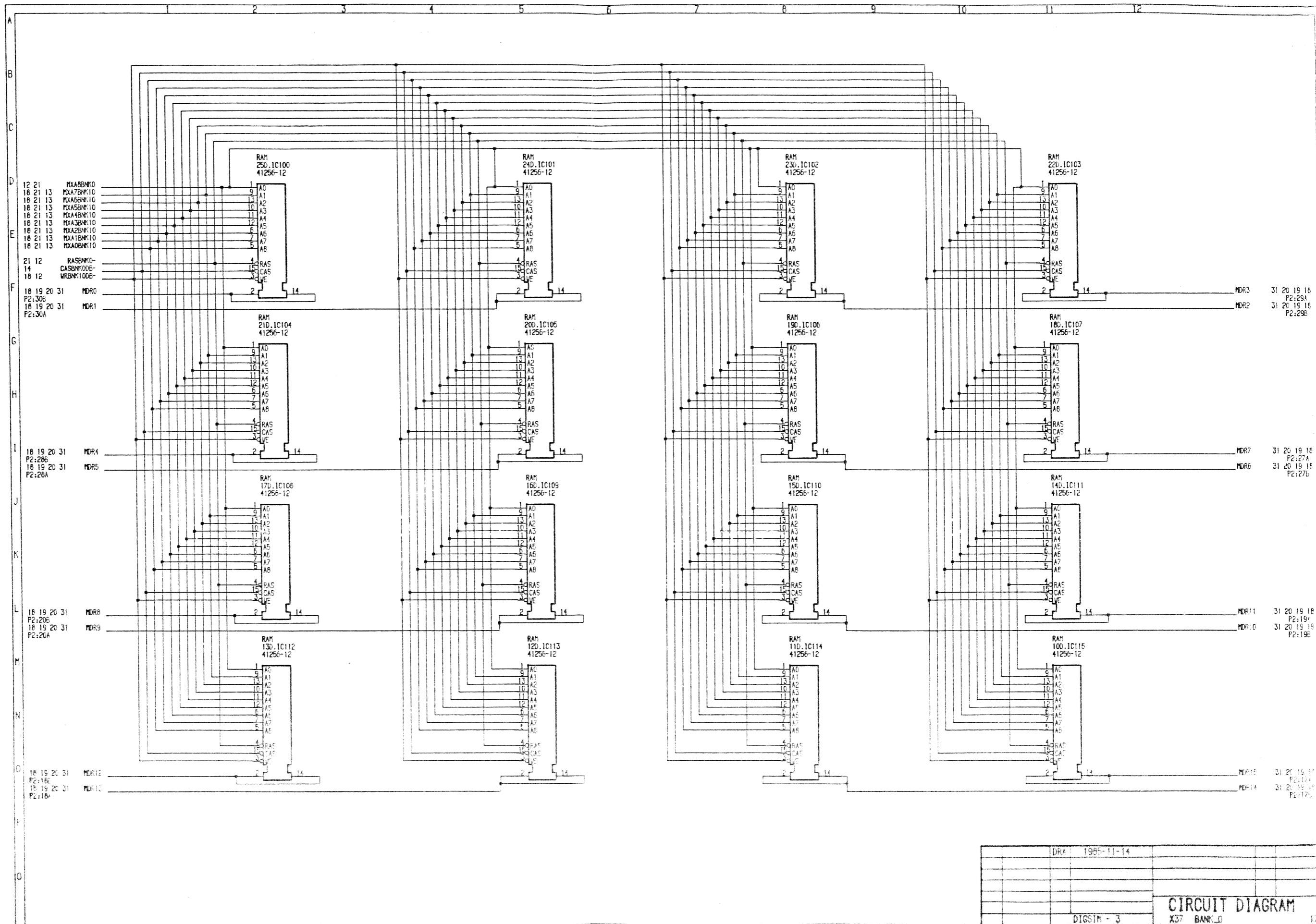
J
K
L
M
1 4 5 14 3
1 3 4 5 14
1 3 4 5 14
1 3 4 14
1 3 4 14
1 3 4 14 7
1 3 4 14 7 11

BA15
BA17
BA18
BA19
BA20
BA21
BA22
BA23
P6:17B
P6:17A
P6:16B
P6:18A
P6:19B
P6:19A
P6:20B
P6:20A

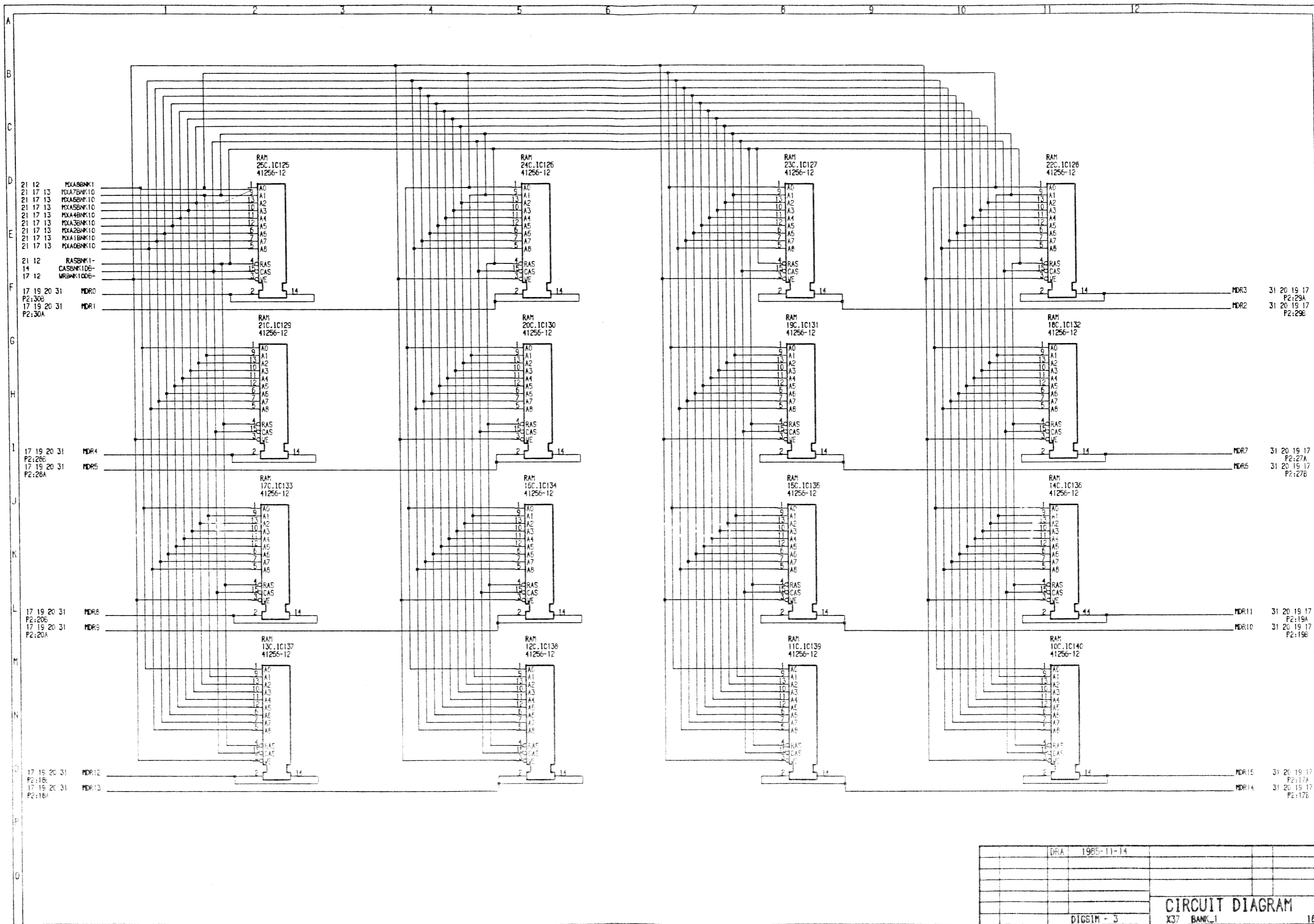
BA8
BA9
BA10
BA11
BA12
BA13
BA14
BA15
P6:29E
P6:29A 12 11
P3:30B
P6:30B 12 11
P3:30A
P6:30A
P6:31B
30P6:31A 25
P6:32E
P6:32A 2E

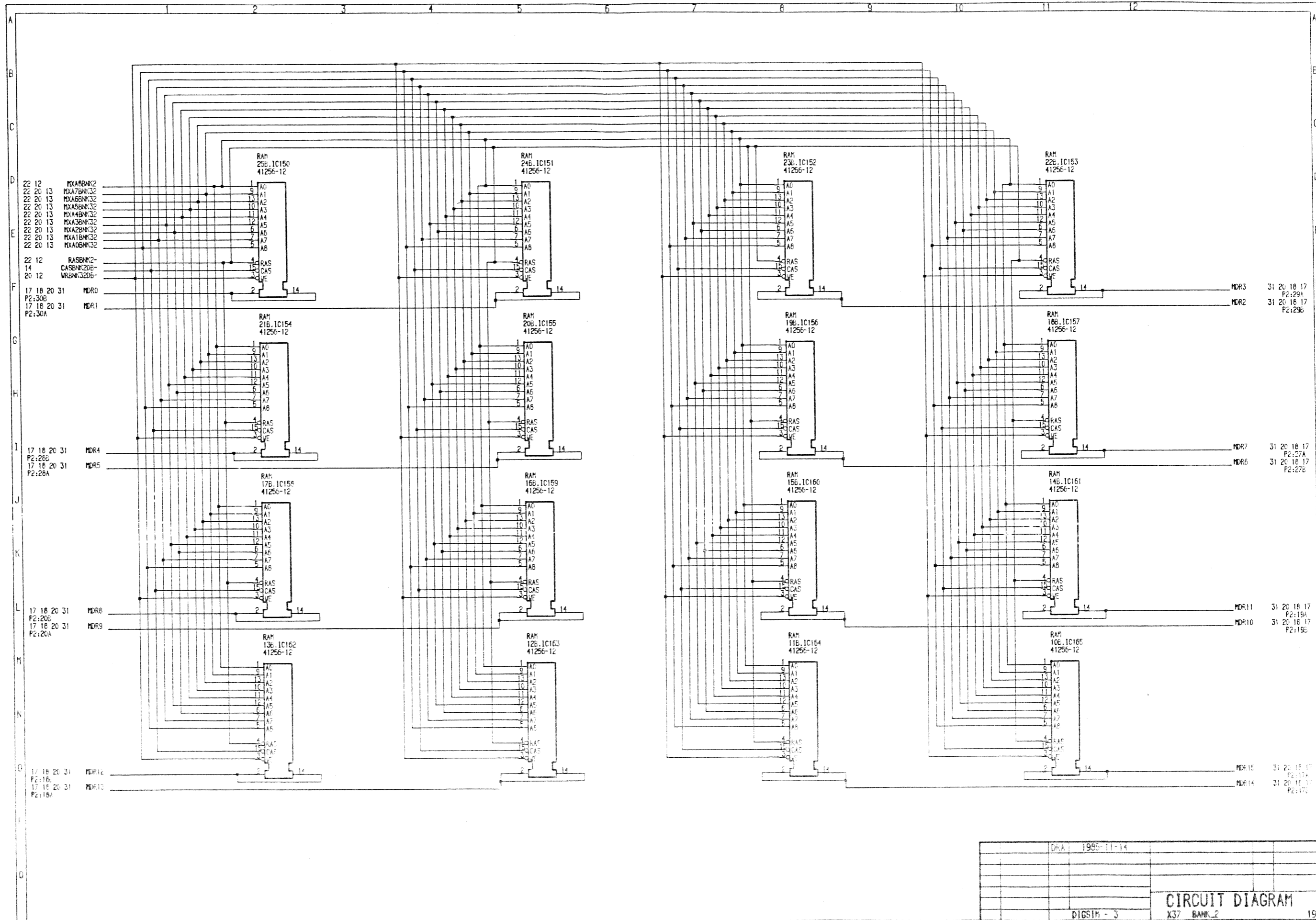
BA1
BA2
BA3
BA4
BA5
BA6
BA7
29 25 24 23
P6:5A
29 25 24 23
P6:5E
24P6:6A 25
24P6:6B 25
24P6:7A
P6:7E
30P6:6A 25

DRA	1985-11-14		
DICSIM - 3		CIRCUIT DIAGRAM	
X37 ADDRESS LATCH		14	

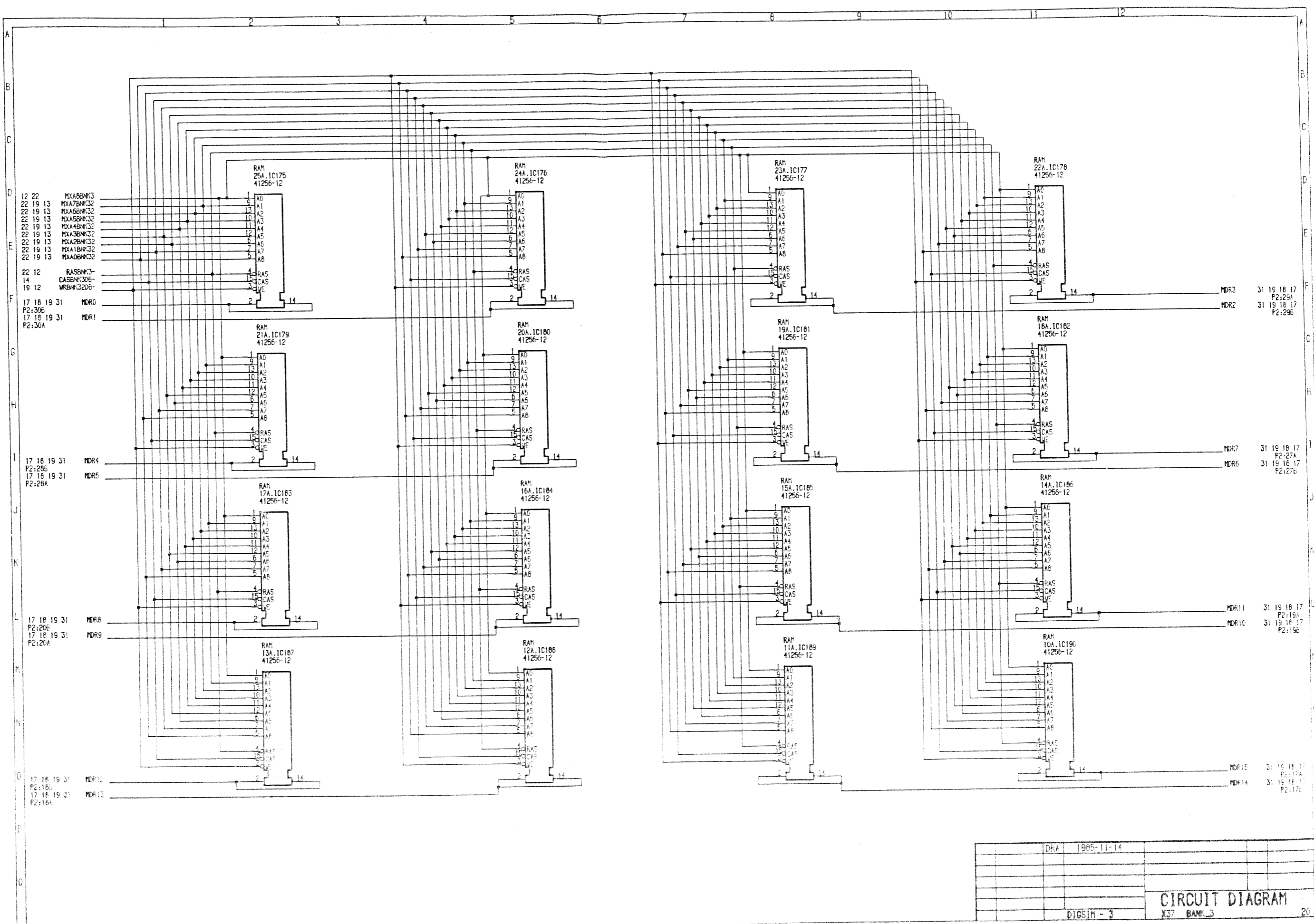


DRA	1985-11-14
CIRCUIT DIAGRAM	
DIGSIM - 3 X37 BANK_0	

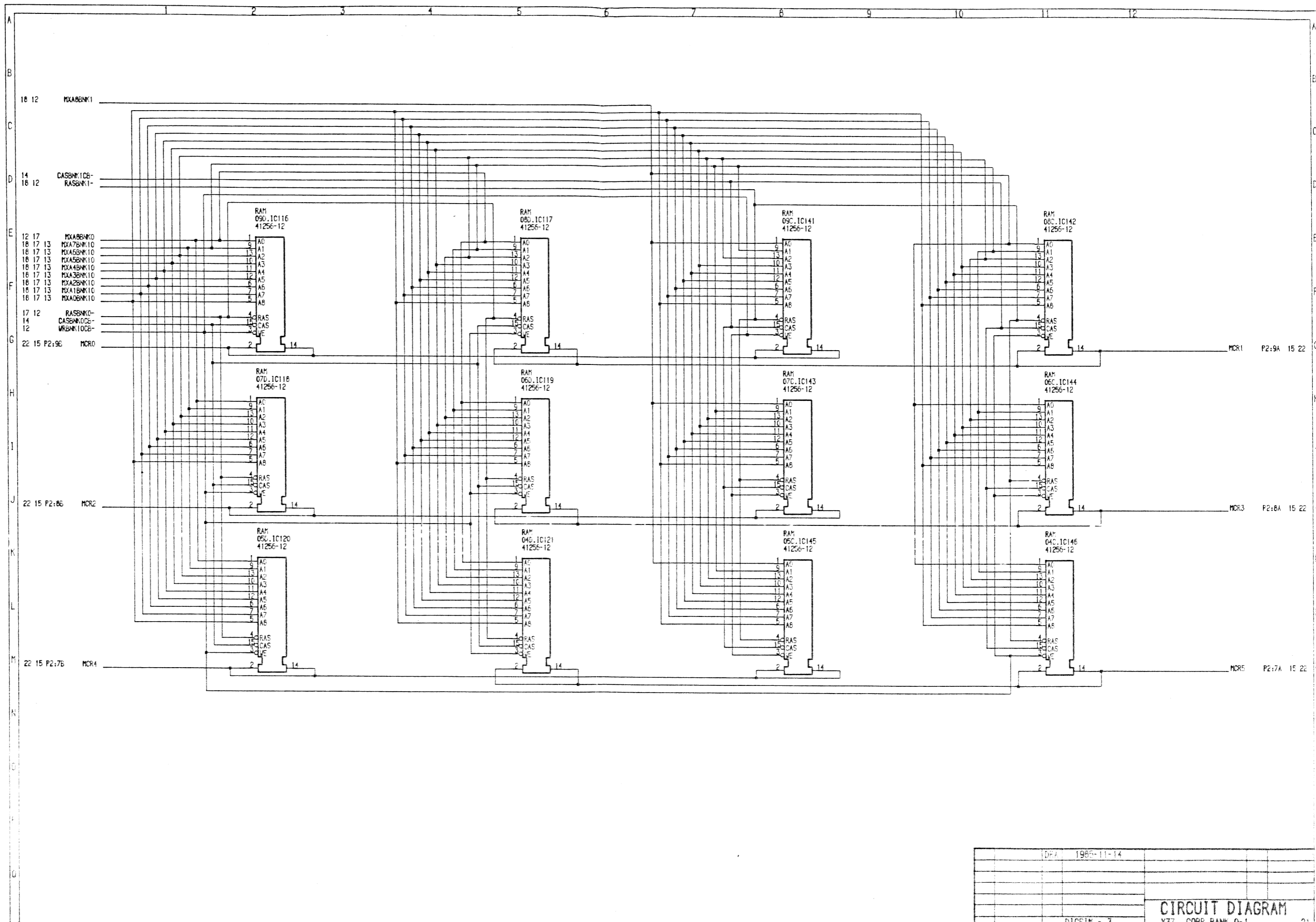




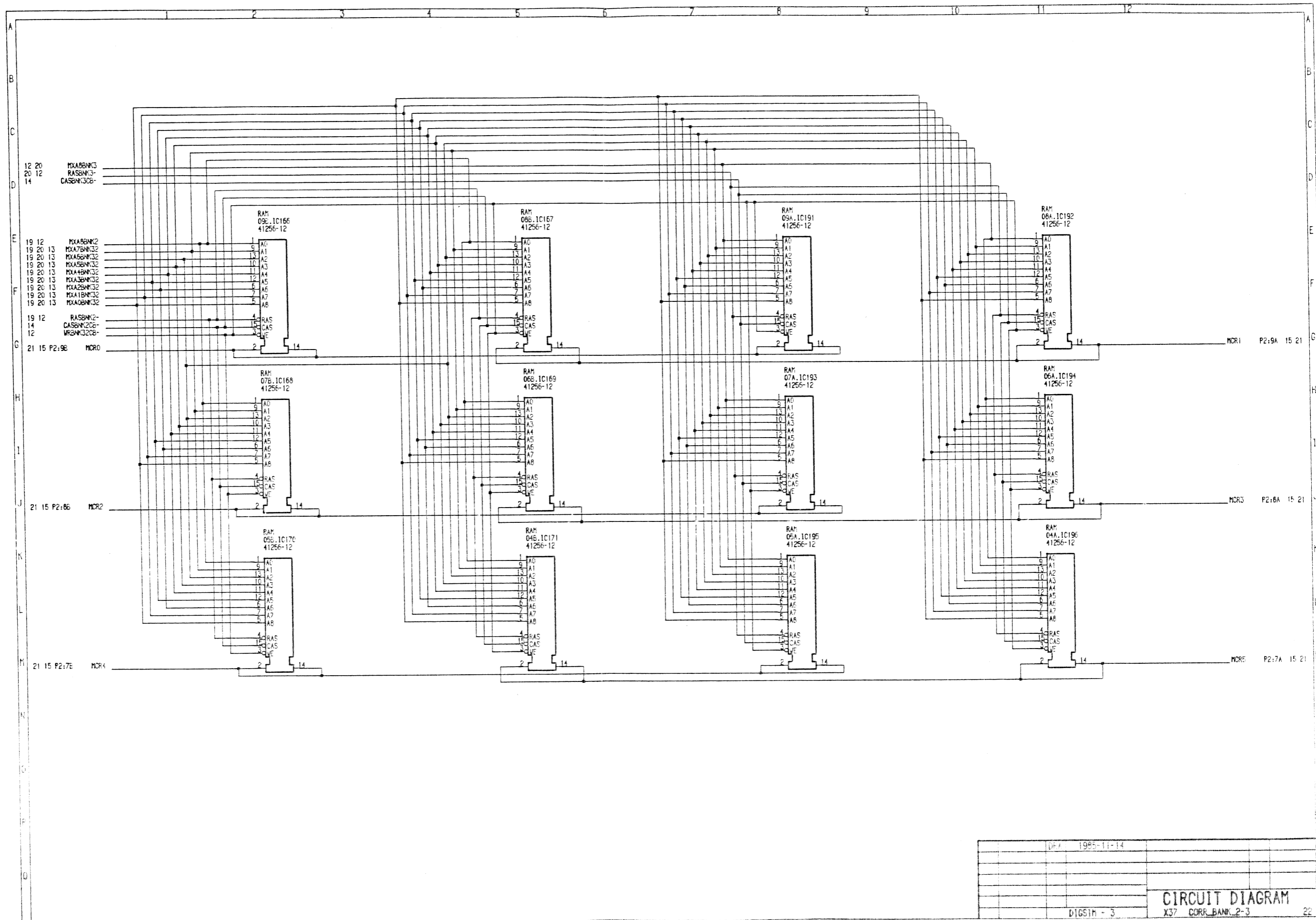
DRA	1985-11-14		
DIGSIM - 3		CIRCUIT DIAGRAM	
		X37 BANK_2	19



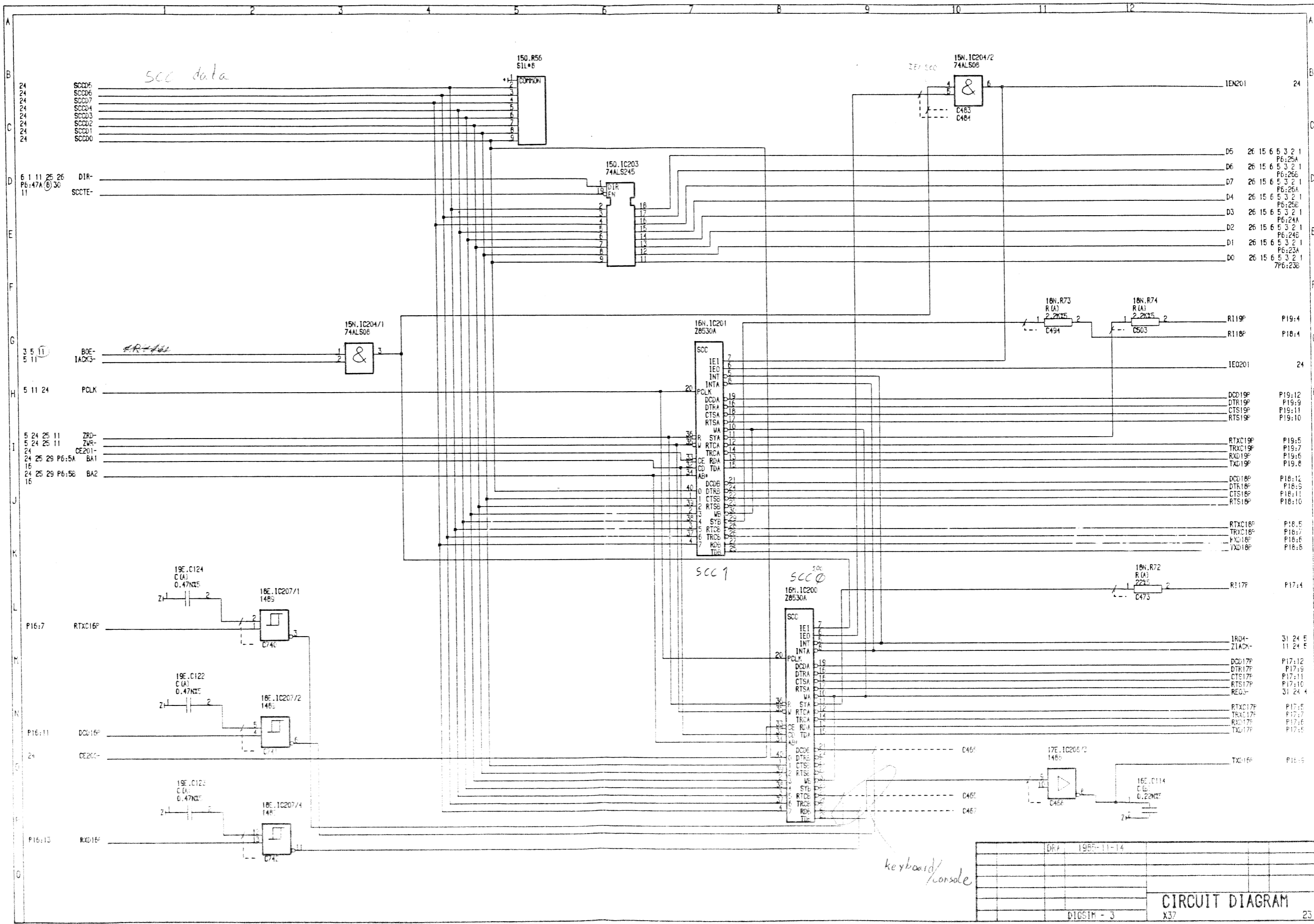
DRA	1985-11-14
CIRCUIT DIAGRAM	
DIGSIM - 3	X37 BANK 3
	20



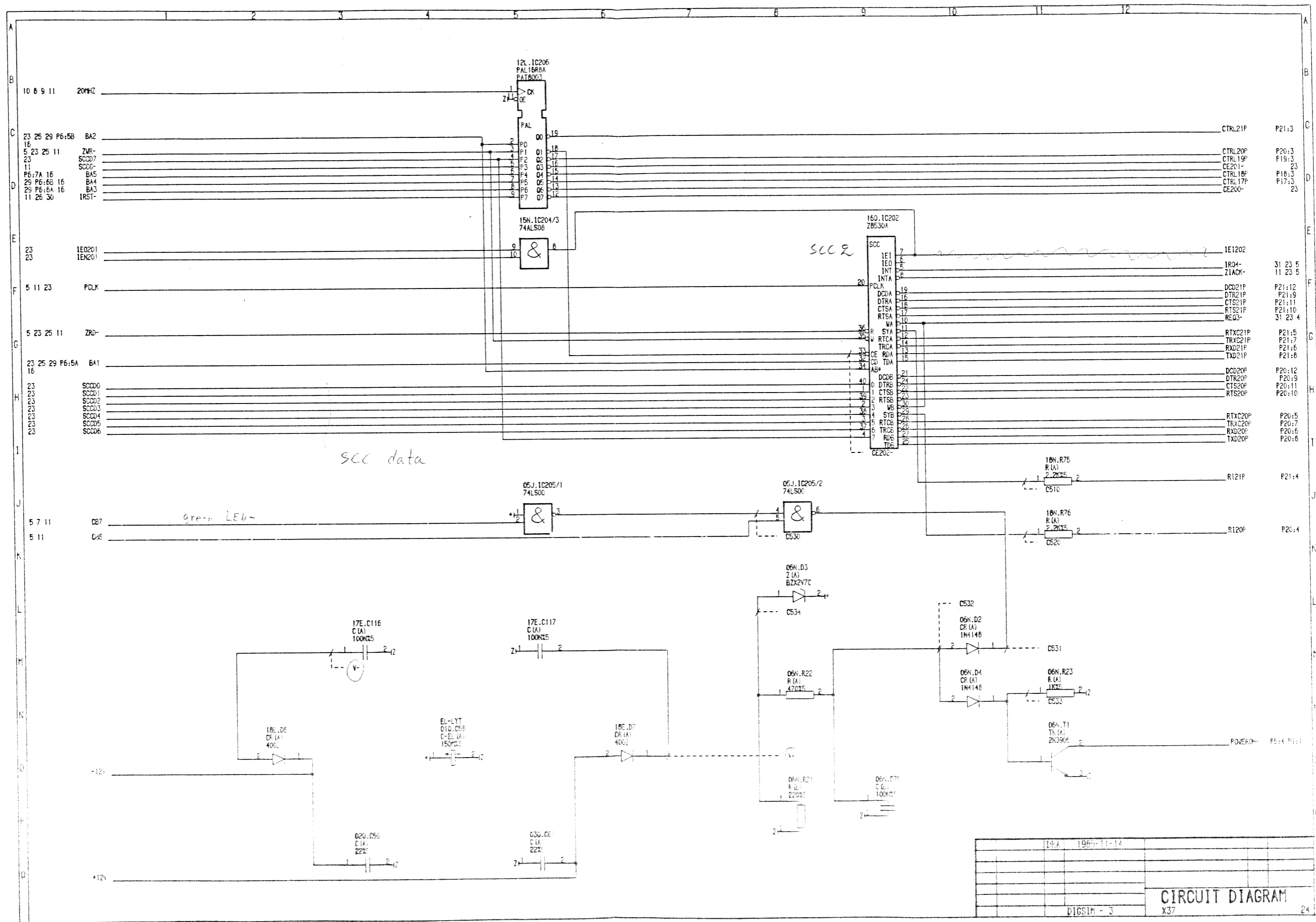
DFA	1985-11-14		
DIGSIN - 3		CIRCUIT DIAGRAM	
		X37 CORR_BANK_0-1	
			21

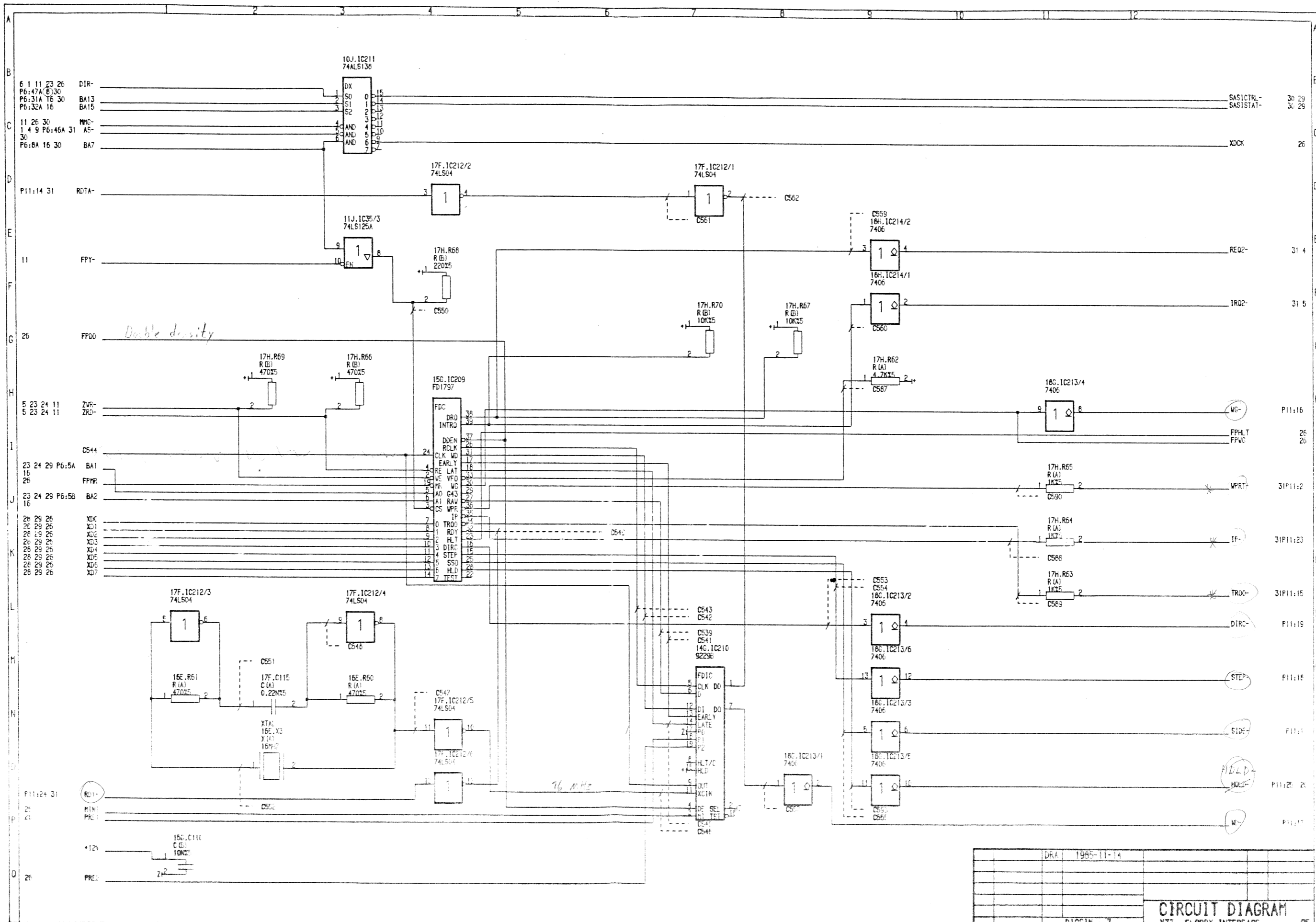


DATE	1985-11-14
DESIGNER	
CHECKED	
APPROVED	
CIRCUIT DIAGRAM	
X37 CORR_BANK_2-3	
DIGS1H - 3	22

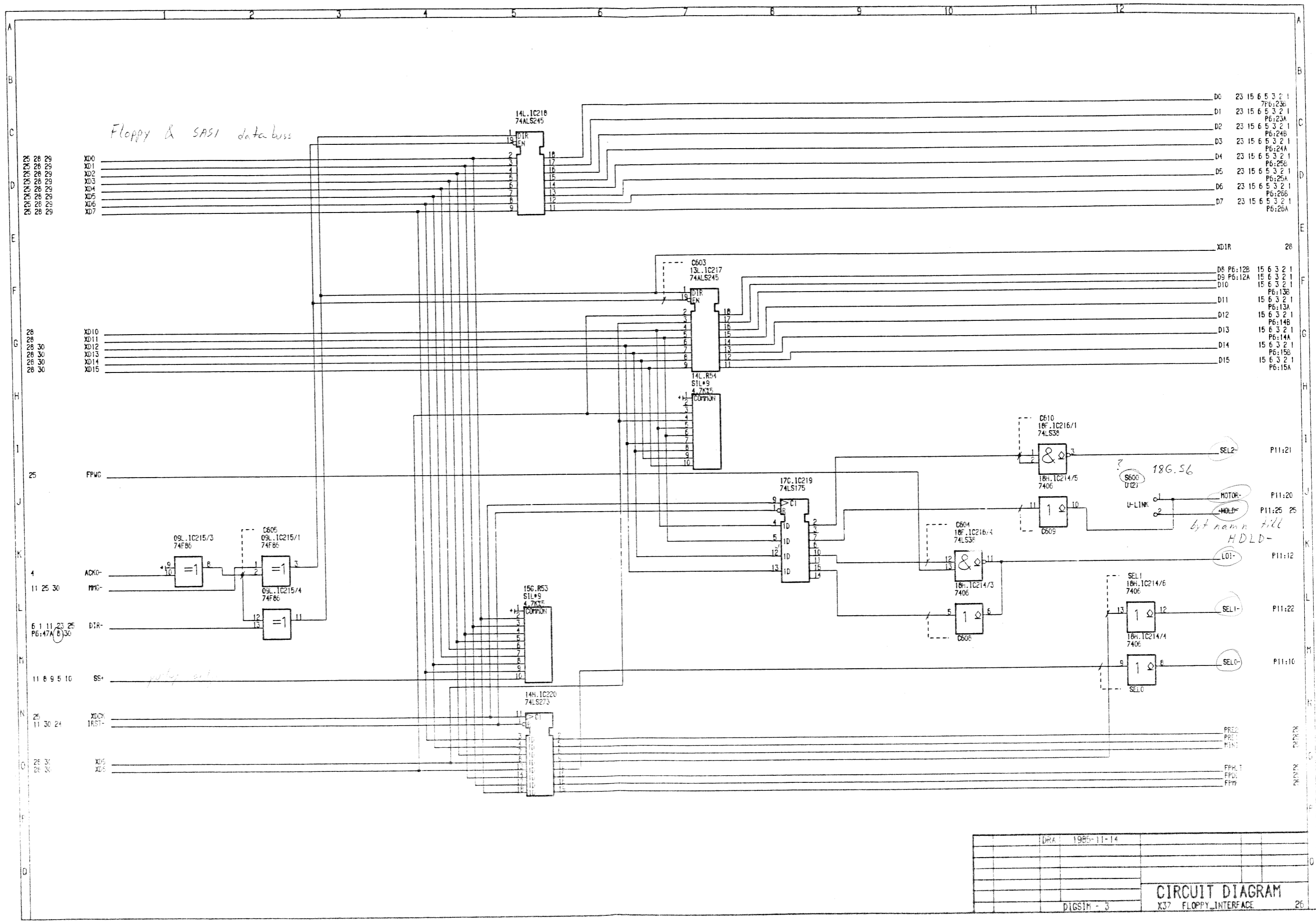


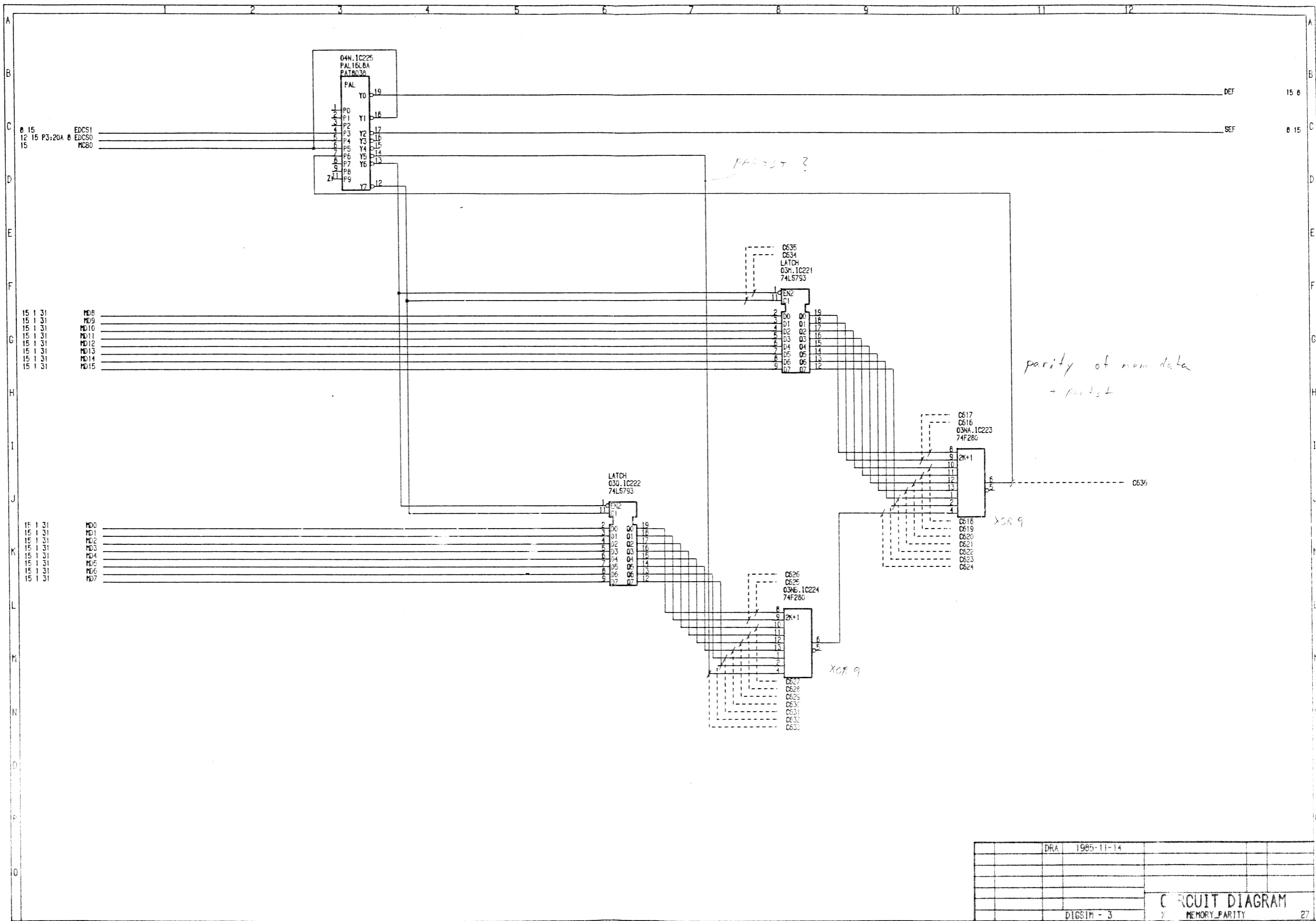
DATE	1985-11-14
DESIGNER	
CHECKED	
APPROVED	
CIRCUIT DIAGRAM	
DIGSIM - 3	X37



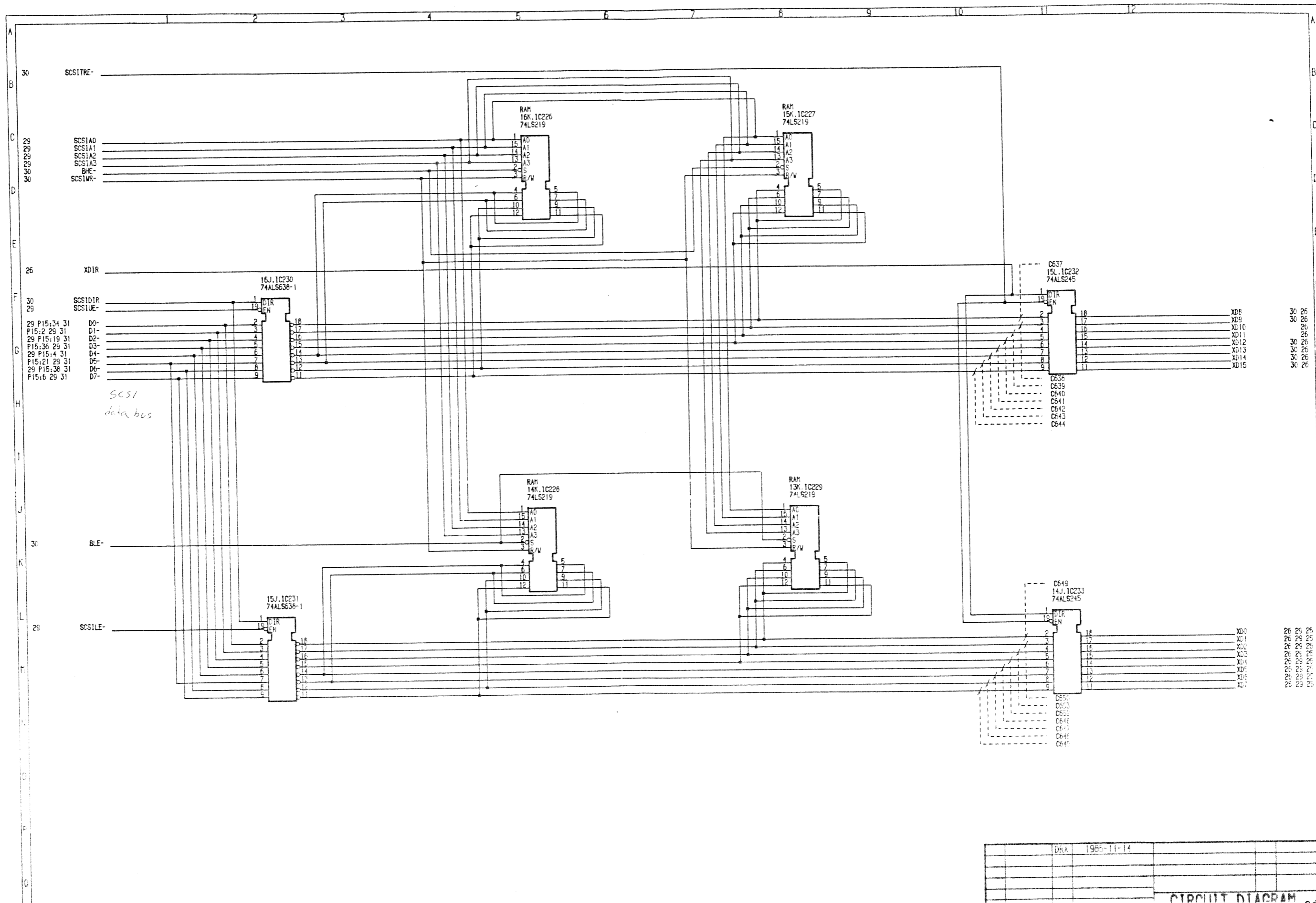


DRAWN	1985-11-14
DESIGNED	3
CIRCUIT DIAGRAM	
X37 FLOPPY INTERFACE	

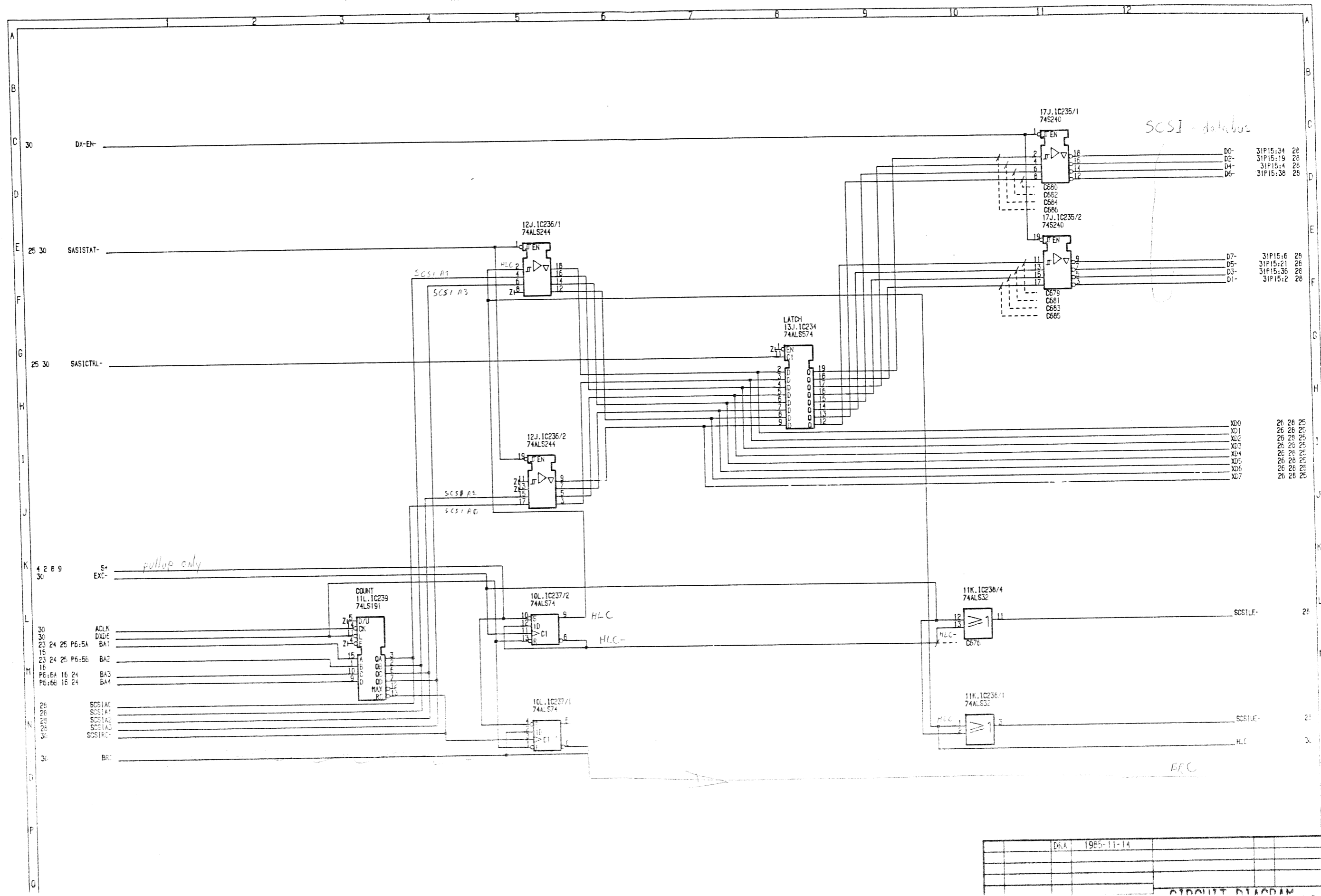


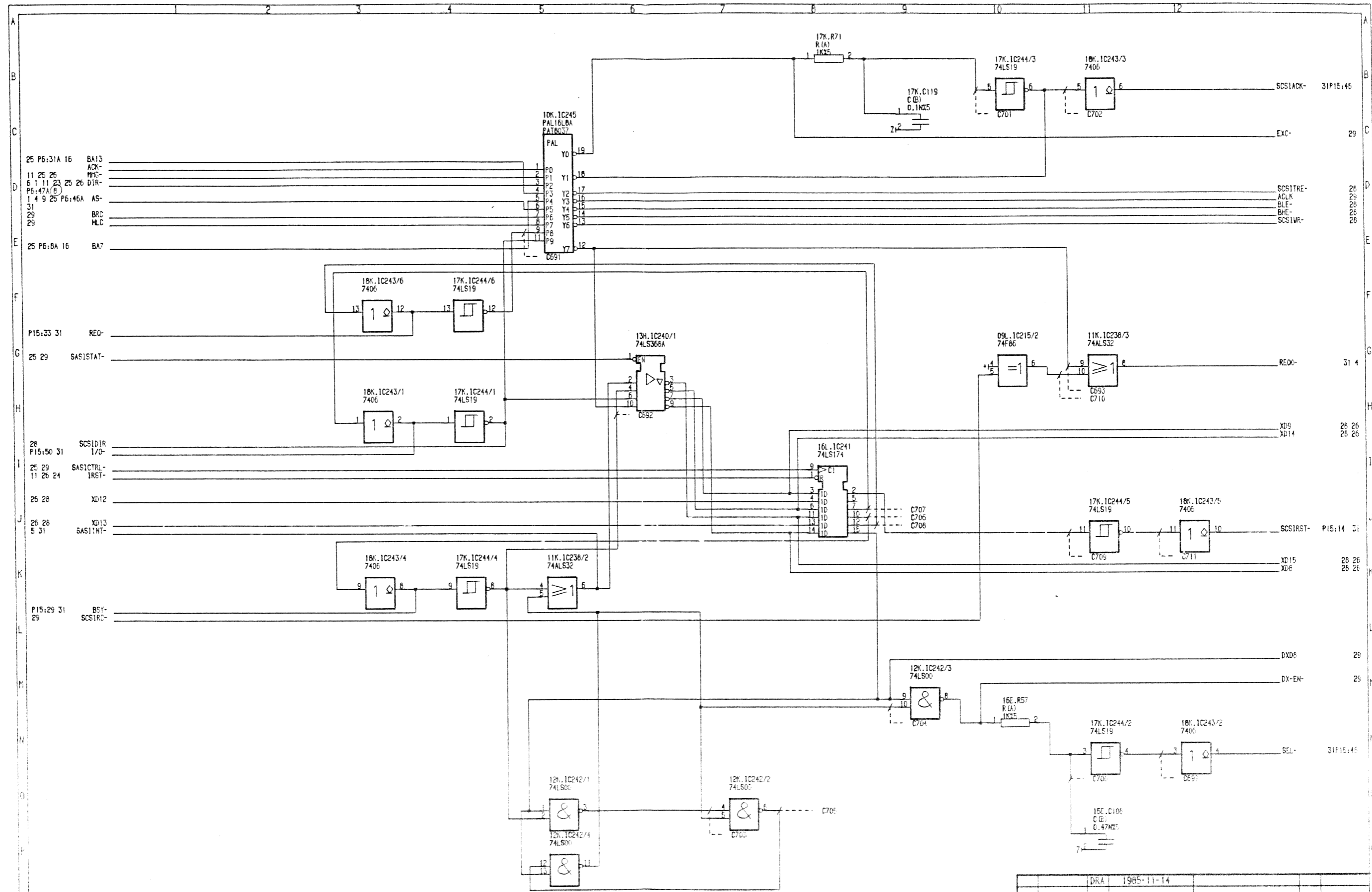


DRA	1985-11-14	
DIGSIN - 3		
CIRCUIT DIAGRAM		
MEMORY_PARITY		27



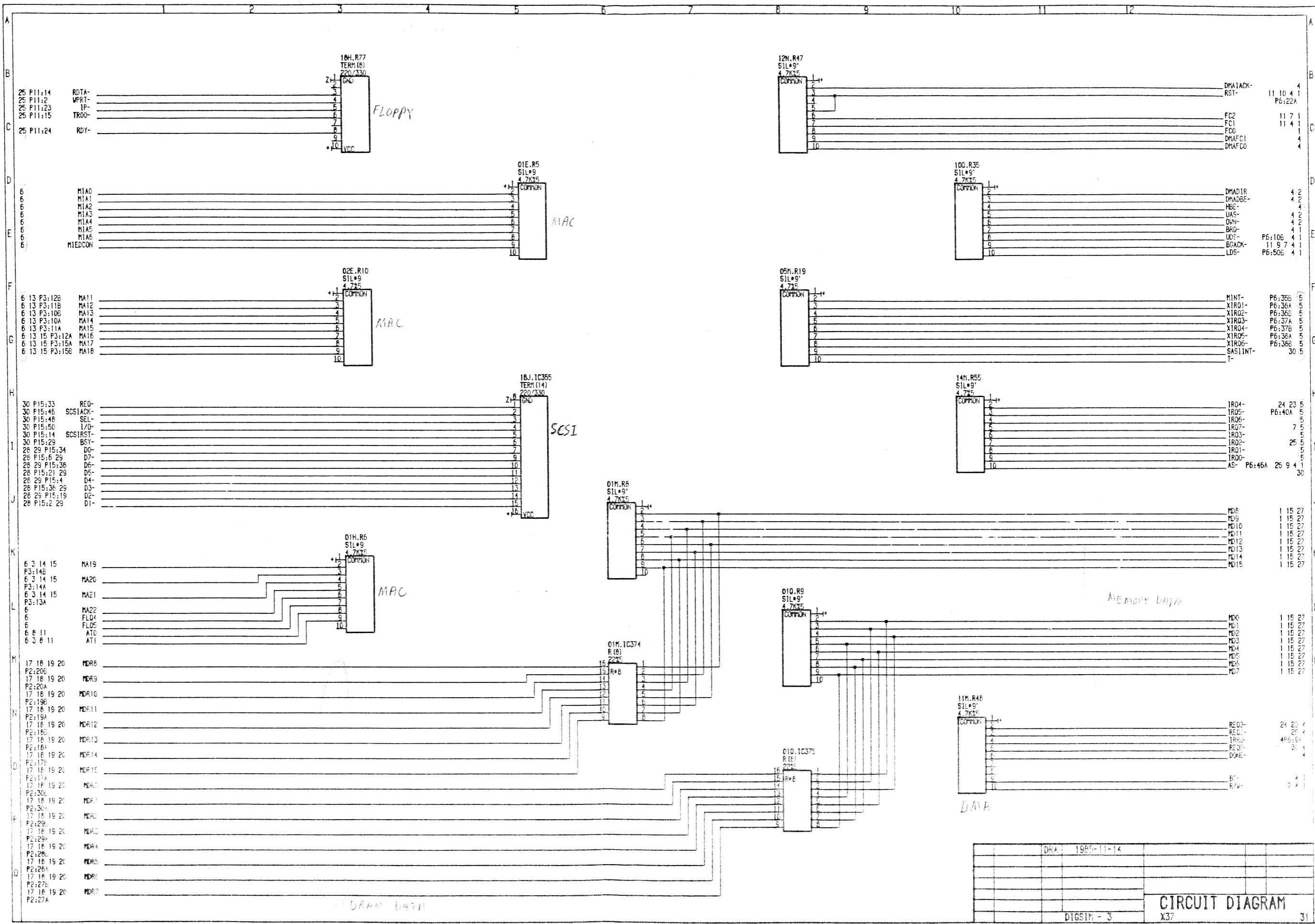
CIRCUIT DIAGRAM
X37 SCS1-INTERFACE 28





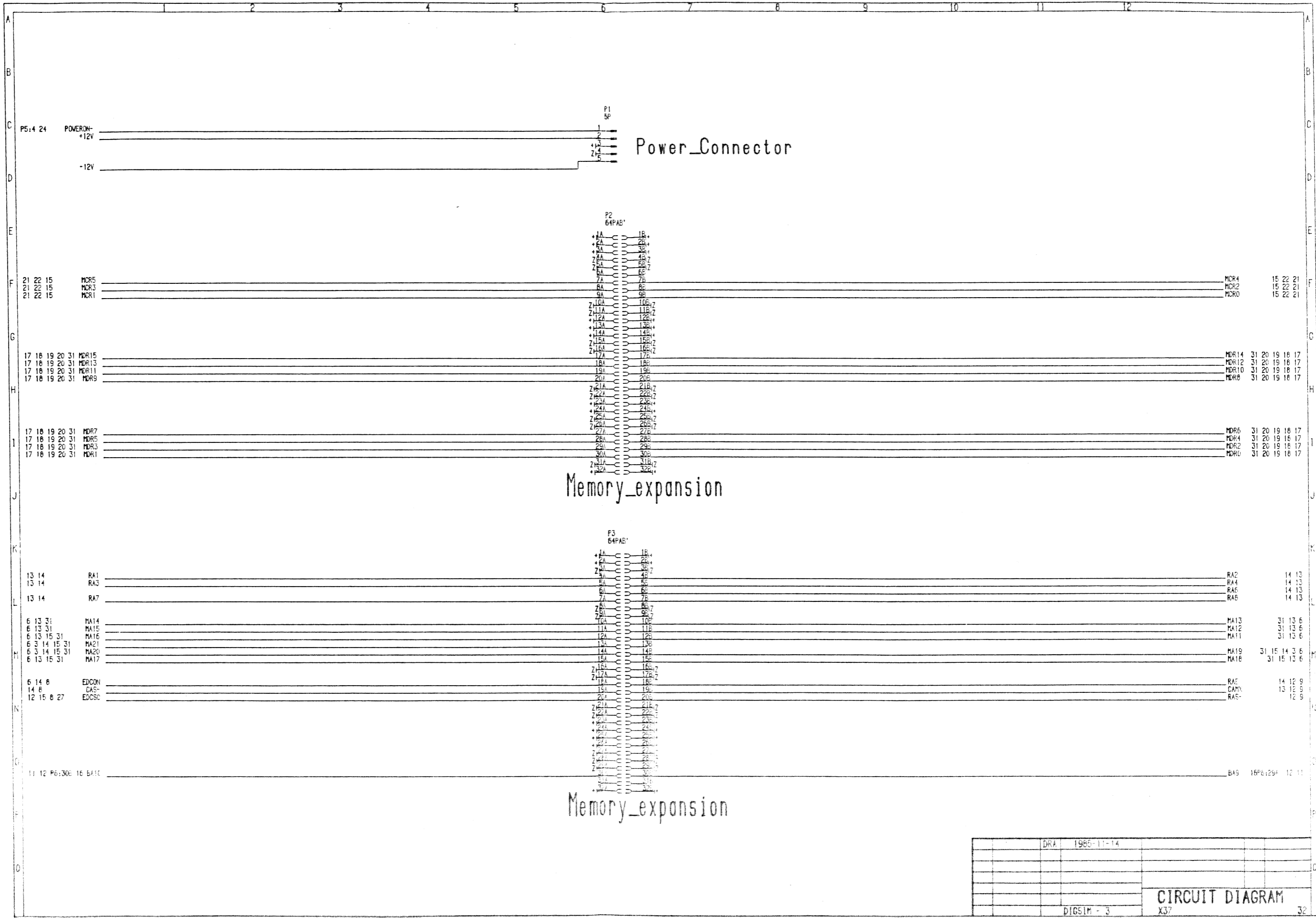
DRA	1985-11-14		

CIRCUIT DIAGRAM
X37 SASI_INTERFACE

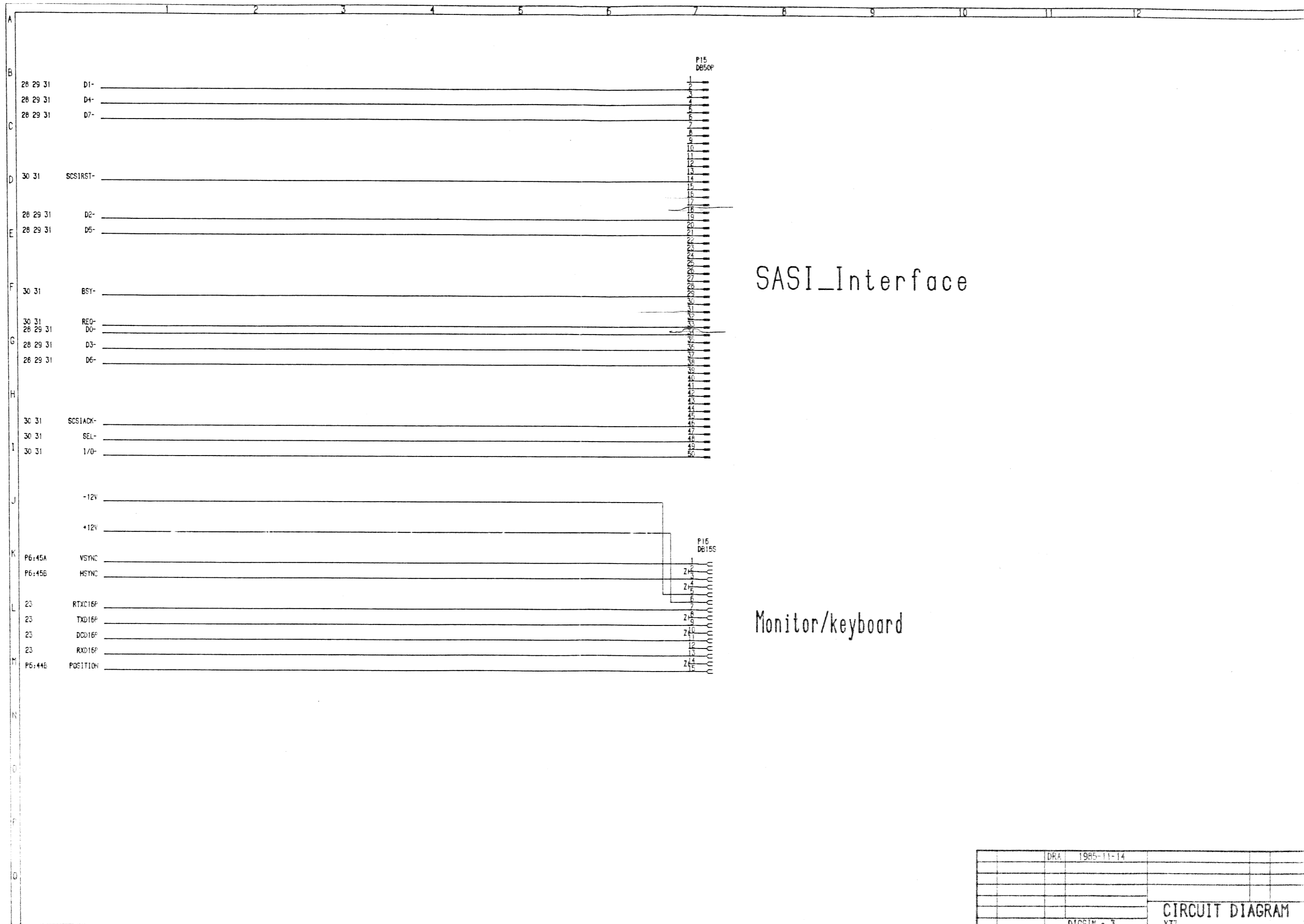


BRAN DATA

DRA	1985-11-14		
CIRCUIT DIAGRAM			
DIGSIM - 3			
X37			



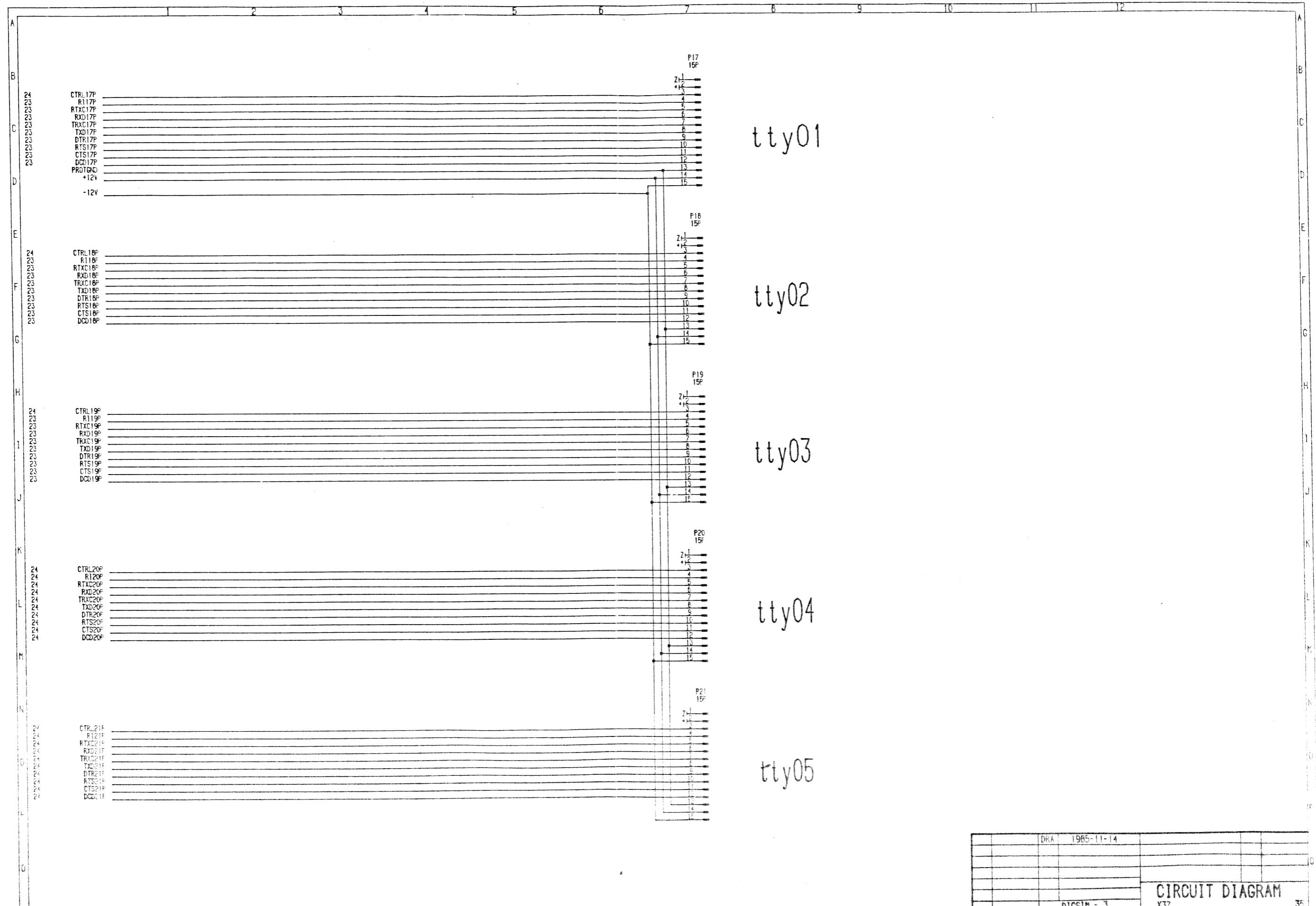
DRA	1985-11-14			
			CIRCUIT DIAGRAM	
DIGSIM - 3	X37			32



SASI_Interface

Monitor/keyboard

DRA	1985-11-14				
CIRCUIT DIAGRAM					
DIGITM - 3 X37					



tty01

tty02

tty03

tty04

tty05

DRAWN	DATE	1985-11-14	REVISED	
DIGSYM - 3				CIRCUIT DIAGRAM
X37				35